Exploration of selector characteristic based on electron tunneling for RRAM array application

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Abstract: Selector is indispensable to suppress leakage current for crossbar array of resistive random access memory. According to the nonlinear requirement, electron tunneling mechanism is firstly attempted. However, earlier studies discovered drawbacks of insufficient current density. This work aims at exploring the idealized characteristic of selector based on Fowler-Nordheim tunneling mechanism by selecting various materials and structures. Thereinto, current density transforms to drive voltage according to corresponding current density standard. Simulation results indicate that metal/insulator barrier and insulator thickness play key roles in determining drive voltage and nonlinearity of the tunneling selectors. Specifically, metal/insulator barrier influence drive voltage and nonlinearity, while insulator thickness mainly influence drive voltage. Thus it can help comprehend restrictions of tunneling mechanism and attempt other improvement directions.

Keywords: selector, RRAM, nonlinearity, F-N tunneling

Classification: Integrated circuits

References

[5] L. Zhang, et al.: “High-drive current (>1 MA/cm²) and highly nonlinear (>10³) TiN/amorphous-Silicon/TiN scalable bidirectional selector with excellent reliability and its variability impact on the 1S1R array performance,”


1 Introduction

Resistive random access memory (RRAM) is considered as one of the most promising candidates for next generation nonvolatile memory. It possesses the potential for high-density storage benefiting from its high scalability, fast switching speed and low power consumption [1]. The RRAM element based on crossbar array can attain feature size of $4F^2$ (F is the line width) or even less when multi-level is realized [2]. Nonetheless, as the array is constructed by pure RRAM element leakage current is inevitable. The leakage current not only consumes much more power that leads to the loss of RRAM advantage, but may also lead to false operation. Thus the huge-scale application of the RRAM storage is seriously impeded. As illustrated in Fig. 1, to suppress leakage current, a nonlinear and bipolar independent device called selector is integrated to the RRAM element, forming the structure called as 1S1R (1-Selector-1-RRAM) [3].

Selector is highly resistive under lower bias while highly conductive under larger bias. As a result, nonlinearity is one of the most significant characteristic. Meanwhile, compared to other mechanisms, tunneling is extensively applied and thoroughly studied semiconductor industry. Thus, selector based on tunneling mechanism is firstly realized because it embodies nonlinear current-voltage relationship with simple metal-insulator-metal structure. Early publishes demonstrated nonlinearity, fast switching speed and excellent endurance property of single selector element [4, 5, 6, 7, 8, 9]. Furthermore, they realized leakage current suppression in single one-selector-one-RRAM (1S1R) cell [4, 5, 6, 7]. However, it is not sufficient only considering nonlinearity without sufficient drive current. When considering the current drive requirements, a customary target set forward is 1 MA/cm². This value is given by the requirement of a selector to deliver a switching current of 1 µA, through a cell size of $10 \times 10$ nm². A lower current drive

ability of a particular selector structure translates into lack of scalability toward 10 nm at the assumed 1 µA operating current. Alternatively, it can translate into only being able to operate the RRAM element at less than 1 µA, for a 10 nm-size cell [8]. Secondly, corresponding to first point, the voltage which current density of 1 MA/cm² requires cannot be much higher than operating voltage of RRAM element. It will be tolerable in single 1S1R cell to verify function while it may lead to voltage incompatibility and operation failure in crossbar array [9]. Finally, nonlinearity is important characteristic influencing array size and power consumption. Thus higher nonlinearity with sufficient drive current means a ideal selector.

This work aims at exploring selector characteristic of F-N tunneling selector by comparing various material and structure parameters. Then it can be guideline for choosing suitable material and structure for selector devices. Meanwhile, it will also help understand restrictions of this mechanism and attempt other improvement directions. The work is organized as follows. The simulation theory and settings are presented in Section 2. The impacts of applied voltage and device parameters on current density and nonlinearity are analyzed in Section 3. The conclusion is given in Section 4.

2 Basis theory for simulation

Selector based on electron tunneling mechanism employs simple metal-insulator-metal structure. Tunneling theory is intensively studied, which provides convenience for simulation before fabricating real device to choose ideal material and structure. Under the circumstance of thin insulator film less than 10 nm or even thinner in MIM structures, tunneling can dominate the current. There exist kinds of electron tunneling mechanisms, such as F-N tunneling, direct tunneling, trap-assisted tunneling, and so on [10]. Here we only consider the F-N tunneling. On one hand, F-N tunneling can occur when insulator thickness is less than 10 nm, while direct tunneling only occurs when insulator thickness is less than 3 nm. On the other hand, trap-assisted tunneling dominates only when there are plenty of traps in the films and the trap quantity can be reduced by implementing other process such as ALD or annealing in vacuum. Meanwhile, if there are plenty of traps in insulator such as HfO₂, it easily shows nonvolatile memory property which is not included in scope. Most importantly, according to previous publishing [4, 5, 6, 7, 8, 9], F-N tunneling is the most prevalent underlying mechanism. As a result, simulation performs on the basis of F-N tunneling mechanism. The F-N tunneling formula is as follows [11],

$$J_{FN} = \frac{q^3}{16\pi^2\hbar\phi_b}F_{in}^2\exp\left(-\frac{4}{3}\frac{\sqrt{2m^*\phi_b^{3/2}}}{\hbar q} \frac{1}{F_{in}}\right)$$

(1)

Where $J_{FN}$ denotes current density of F-N tunneling, $q$ denotes electron charge, $\hbar$ denotes reduced Plank’s constant, $m^*$ denotes effective mass of insulator electron, $\phi_b$ denotes barrier height on the interface between metal and insulator, and $F_{in}$ denotes electric field intensity. Herein $m^*$ of the same material is constant regardless of the impact of thickness. Since 1998, Eric M. Vogel has verified the validity of this approximation method [12].
In the meantime, selector nonlinearity is a gauge of the selector ability to evaluate the current limiting ability and array size. Half-bias nonlinearity is defined as the ratio between the current at the applied voltage (i.e. $V_{app}$, at which the selector reaches it maximum drive current required to write the resistive memory element) and the current when the selector is biased at $1/2 V_{app}$, which is expressed as [13]:

$$NL_{1/2} = J(V = V_{app})/J(V = V_{app}/2)$$  \hspace{1cm} (2)

Where $NL_{1/2}$ denotes nonlinearity of half bias scheme, $J$ denotes current density.

Zhang et al. investigates the selector characteristics on the overall 1S1R cell performance and extracted selector requirements using parameterized characteristics for a 1 Mbit array, taking into account the read/write margins and power consumption constraints [14]. The read margin is strongly affected by the selector nonlinearity while write margin strongly depends on the selector $V_{app}$. If the switching current of RRAM element is 20 uA, a selector with nonlinearity of about 4000 is required to meet all the constraints [14]. However, when it comes to the selector variability and RRAM element variability, the requirements of half-bias nonlinearity increases to 8000 [15].

### Table I. Work function of different metal materials

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</thead>
<tbody>
<tr>
<td>Work function (eV)</td>
<td>4.45</td>
<td>4.55</td>
<td>4.7</td>
<td>5.2</td>
<td>5.3</td>
</tr>
</tbody>
</table>

### Table II. Relative parameters of different dielectric materials

<table>
<thead>
<tr>
<th>Insulator</th>
<th>Electron affinity (eV)</th>
<th>Effective electron mass</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ta$_2$O$_5$ [21]</td>
<td>3.2</td>
<td>0.3</td>
</tr>
<tr>
<td>TiO$_2$ [22]</td>
<td>2.95</td>
<td>0.3</td>
</tr>
<tr>
<td>HfO$_2$ [23]</td>
<td>2.65</td>
<td>0.17</td>
</tr>
<tr>
<td>Al$_2$O$_3$ [23]</td>
<td>1.35</td>
<td>0.35</td>
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</tbody>
</table>

Detailed material parameters of selected metal and insulator are depicted in Table I and Table II respectively. They are all common and widely used material in RRAM field. Metal from Table I as electrode and insulator from Table II forms the MIM structure with insulator thickness varying from 3 to 10 nm.

### 3 Results and discussion

#### 3.1 Relationship between selector characteristics and applied voltage

Firstly, the relationship between selector characteristic and applied voltage is studied. Take Pt/TiO$_2$/Pt as example, which is studied by Woo et al. and they verified the underlying mechanism is F-N tunneling [9]. Simulation results basically fitted with experiment results. As illustrated in Fig. 2, current density increase while nonlinearity decrease with increasing voltage regardless of insulator thickness. On one hand, increasing voltage bends energy band and enhances tunneling
probability, leading to increasing current density. On the other hand, tunneling probability of half voltage and voltage are not changing in the same value. When the current density is awfully low, lower voltage increase will improve current density obviously. As a result, nonlinearity decreases when voltage increases. However, published papers usually emphasize the nonlinearity while they neglected the current density. As a consequence, there exists contradiction between current density and nonlinearity.

3.2 Relationship between selector characteristics and device parameters

Fig. 3 demonstrates the relationship between selector characteristics and device parameters – insulator thickness and barrier height. Voltage is set to 4 V. Insulator is TiO2, and different electrode leads to various barrier height. According to the results, current density decreases with barrier height and insulator thickness. Because increasing barrier height and insulator thickness will reduce tunneling probability and current density. While nonlinearity increases with insulator thickness and barrier height. These relationships are all determined by F-N tunneling intrinsic properties.

Fig. 2. Relationship between selector characteristics and applied voltages

Fig. 3. Relationship between selector characteristics and device parameters
3.3 Exploration of idealized selector characterization based on F-N tunneling

To explore idealized selector characterization, current density of $10^6 \text{A/cm}^2$ is set as the standard. Because current density is firstly guaranteed, then nonlinearity is considered as meaningful. According to above study, barrier height determined by

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Fig. 4. Trends of drive voltage and nonlinearity with various barrier height and thickness under different effective mass (a) $m^* = 0.1$, (b) $m^* = 0.2$, (c) $m^* = 0.3$, (d) $m^* = 0.4$
material and insulator thickness deeply impacts the characterization. Thus drive voltage for sufficient current density and corresponding nonlinearity are studied to choose appropriate material and structure. In the meantime, effective mass of insulator in tunneling mechanism is variable among various insulator materials. Hence, according to Table II, only four set of effective mass are chosen and the study results are also suitable for other insulator thickness.

As illustrated in Fig. 4, to meet the requirement of $10^6$ A/cm$^2$ current density, desired drive voltage increase with barrier height and insulator thickness. This is owing to the negative correlation between tunneling probability and barrier height and insulator thickness. In order to increase the probability, larger voltage is needed to severely distort the energy band. Meanwhile, corresponding nonlinearity is not variable to insulator thickness while increases with increasing barrier height. In addition, maximum nonlinearity is about $\sim 10^3$ which is not enough for high-density arrays.

In summary, drive voltage and nonlinearity both positively correlate to the barrier height. In addition, drive voltage positively correlate to insulator thickness as well, while nonlinearity is independent of insulator thickness. As a result, in order to guarantee the drive voltage and nonlinearity, there exists a conflict. According to above results, when we design selectors we should take the tradeoff into consideration.

### 4 Conclusion

In this work, it is proved material and structural parameters can have influence on selector performance through numerical calculation. Barrier height between metal and insulator affects drive voltage and nonlinearity while insulator thickness mainly impacts drive voltage. Actually, this work aims at posing a guideline for choosing material and thickness to fabricate MIM selectors satisfying requirements. There exists tradeoff between high nonlinearity and low drive voltage for MIM selectors based on electron tunneling mechanism. It is suggested to choose materials and structural parameters in consideration of the most important requirement. In the meantime, the proposed method can be used to choose suitable parameters for experiments, which is economical and convenient of numeric simulation in advance of fabrication procedure.

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