Design of a broadband Ka-band MMIC LNA using deep negative feedback loop

Gang Wang, Wei Chen, Jiarui Liu\textsuperscript{a)}, Jiongjiong Mo, Hua Chen, Zhiyu Wang, and Faxin Yu
School of Aeronautics and Astronautics, Zhejiang University,
38 Zheda Road, Hangzhou, China
\textsuperscript{a}) jrliu@zju.edu.cn

Abstract: In this paper, we present a broadband Ka-band LNA using 0.15-µm GaAs pseudomorphic high electron mobility transistor (pHEMT) process. By using bandwidth enhancement techniques and deep negative feedback technology, the LNA achieves relatively broadband performances. The LNA attains 20 dB small signal gain from 25 to 40 GHz and shows a measured noise figure of 2.8 dB from 25 to 40 GHz with 230-mW dc power consumption. The input and output return loss of the LNA is less than 8 dB, which is competitive compared with other published Ka-band LNAs. The size of the chip is 2.5 mm × 1.2 mm.

Keywords: Ka-band, low noise amplifier (LNA), MMIC, broadband

Classification: Microwave and millimeter-wave devices, circuits, and modules

References


1 Introduction

The Atacama large millimeter/sub-millimeter array (ALMA) is a state-of-art ground based astronomical telescope projected to explore the outer space in the millimeter and sub-millimeter regime. It enables scientists to conduct further research into the building blocks of stars, planetary systems, galaxies and life itself by sensing detail images of planets and galaxies in the observable universe [1].

To improve the image resolution with the weak receiving power, a highly sensitive receiver system is required. The signal-to-noise ratio of the overall receiver is dominated by the noise and gain of the first-stage low noise amplifier (LNA). Most of the LNAs with high gain and low noise performance are designed in GaAs- or InP-based HEMT processes. Although the InP-based HEMT devices demonstrate better millimeter wave (MMW) characteristics than GaAs HEMT devices, GaAs HEMT is a mature microwave integrated circuit process that strikes a balance between circuit performance and fabrication cost [2].

In this paper, we present a Ka-band LNA for radio astronomy applications using 0.15-µm GaAs pHEMT process. The design targets of the LNA are 20-dB small signal gain and 3-dB noise figure to make sure that the total noise performance of the receiver system is guaranteed. Compared with the reported LNAs, this work shows high gain and low noise performances in Ka-band with acceptable dc power consumption.

2 Design principle and approach

The design of broadband LNA comprises two basic parts, the basic targets of the LNA and the bandwidth enhancement methods. The LNA adopts deep negative feedback loop to help enhancing the stability and broadening the bandwidth.

2.1 Basic design concern

The overall schematic of the Ka-band LNA is shown in Fig. 1. The LNA is based on four-stage common source structure with the matching networks realized by microstrip lines and reference ground using backside metal.

Stability is an important figure of merit for any amplifier design. In order to achieve unconditional stability, it is extremely important that the matching is done properly, otherwise large standing waves will appear. The large standing waves will
not only destroy the amplifier performance but also result in amplifier instability which makes the amplifier unusable at all [3]. Other than doing the input and output matching networks in a multi-stages LNA, it is also important that the inter-stage matching networks are properly designed as well.

A two-finger transistor with total gate width of 100 µm is applied to four stages for sufficient gain and low noise figure. The major concern of the input matching is to minimize the noise figure while sustaining enough power gain. It is well-known that source degeneration technique can meet the goal by making the minimum noise figure point (Γ_{opt}) and input conjugate point (Γ_{MS}) point close to each other on Smith chart [4]. In this work, two 100-µm microstrip lines are connected to the source of the first-stage transistor. In addition, with the source degeneration and the loss of the matching networks, the circuit can be stabilized. A good design will have an amplifier with minimum noise figure at first stage and low-pass network at the output in order to reduce the spurious and high harmonic content. Band-pass networks at the input and inter-stages will provide good broadband matching and gain forming to increase the stability.

2.2 Bandwidth enhancement techniques
To broaden the bandwidth of the LNA, two techniques are used. One is the π-type matching network as shown in Fig. 2. Since an open stub is equivalent to a shunt capacitor and a short stub is equivalent to a shunt inductor, the π-type matching network can be thought as a LC tank [5]. By lowering the Q factor of a LC tank, bandwidth can be extended. The other technique is the deep negative feedback loop. The resistor, capacitor and microstrip between drain and gate of the transistor form a feedback network. The aim of the network is to mismatch inter-stage
matching networks to compensate for the changes with frequency of gain. In other words, the matching networks are designed to match at different frequency bands at different stages.

3 Implementation and measurements

The chip is fabricated with 0.15 µm GaAs pHEMT process. Fig. 3 shows the photograph of the Ka-band LNA. The chip size is 2.5 mm × 1.2 mm. Dc bias is fed through bond pad, and the noise figure is measured by spectrum analyzer (R&S FSU43). The S-parameters are tested by vector network analyzer (Agilent PNA 5224A) using GSG co-planar microwave probes on probe station.

Fig. 3. Photograph of the fabricated LNA.

Fig. 4. Input and output return loss of the Ka-band LNA

Fig. 5. Gain of the Ka-band LNA
As shown in Fig. 4, the fabricated Ka-band LNA exhibits less than −8 dB input and output return loss at frequency ranging from 25 to 40 GHz. Fig. 5 demonstrates the average gain of 20 dB at this frequency range. Fig. 6 shows the noise figure of the LNA in the whole band, which is less than 2.8 dB. The bias voltage of Vd is 5 V and the working current is 46 mA. Table I summarizes the performance comparison of this work with other published LNAs working at close frequency ranges. The characteristics of the LNA includ return loss, gain, noise figure and total chip area consume.

**Table I.** Performance comparison of relevant LNAs

<table>
<thead>
<tr>
<th>Ref</th>
<th>Device Tech</th>
<th>Freq (GHz)</th>
<th>Gain (dB)</th>
<th>Noise Figure (dB)</th>
<th>Return Loss (dB)</th>
<th>Size (mm²)</th>
</tr>
</thead>
<tbody>
<tr>
<td>[6]</td>
<td>0.15-µm GaAs pHEMT</td>
<td>28.5~50.5</td>
<td>23</td>
<td>3.8</td>
<td>5</td>
<td>2.0 × 1.5</td>
</tr>
<tr>
<td>[7]</td>
<td>0.2-µm InP HEMT</td>
<td>23~49</td>
<td>11</td>
<td>2.5</td>
<td>5.5</td>
<td>2.3 × 1.5</td>
</tr>
<tr>
<td>[8]</td>
<td>0.15-µm GaAs mHEMT</td>
<td>32~50</td>
<td>29</td>
<td>3.1</td>
<td>5</td>
<td>2.0 × 1.0</td>
</tr>
<tr>
<td>[9]</td>
<td>0.15-µm GaAs mHEMT</td>
<td>30~50</td>
<td>20</td>
<td>3.7</td>
<td>4</td>
<td>2.0 × 1.0</td>
</tr>
<tr>
<td>[10]</td>
<td>90-nm CMOS</td>
<td>30~42</td>
<td>18</td>
<td>5</td>
<td>7</td>
<td>0.6 × 0.48</td>
</tr>
<tr>
<td>[11]</td>
<td>0.1-µm GaAs pHEMT</td>
<td>27~45</td>
<td>25</td>
<td>3.1</td>
<td>5</td>
<td>2.0 × 1.0</td>
</tr>
<tr>
<td>This work</td>
<td>0.15-µm GaAs pHEMT</td>
<td>25~40</td>
<td>20</td>
<td>2.8</td>
<td>&gt;8</td>
<td>2.5 × 1.2</td>
</tr>
</tbody>
</table>

**4 Conclusion**

In this letter, a high-performance broadband Ka-band LNA is reported. The MMIC LNA is fabricated with 0.15-µm GaAs pHEMT process. A deep negative feedback topology is employed in the last two stages to extend the bandwidth and to achieve
stability. The measurement data reveals that the LNA has 20 dB gain, and more than 8 dB input and output return loss. Noise figure in the whole working frequency is less than 2.8 dB. The measured results show low noise figure and high gain of the presented monolithic LNA.

Acknowledgments

This work was supported by the National Science Foundation of China under Grant 61401395 and 61604128, the Scientific Research Fund of Zhejiang Provincial Education Department under Grant Y201533913, and the Fundamental Research Funds for the Central Universities under Grant 2016QNA4025.