A quadrature voltage-controlled oscillator using phase-adjusting architecture for suppressing phase noise

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Abstract A low-phase-noise 25-GHz quadrature voltage-controlled oscillator (VCO) using 180-nm TSMC CMOS is presented. To equalize the oscillation frequency to the LC-resonant frequency, a phase shifter at a transistor gate adjusts a phase delay due to an RC delay in a transistor. This phase-adjusting architecture extends operation bias range and suppresses phase noise by 6 to 9 dB compared with a conventional differential VCO.

key words: phase noise, voltage-controlled oscillator, gate delay, phase-adjusting architecture

Classification: Integrated circuits

1. Introduction

High-bit-rate wireless communications demand high carrier frequencies [1], such as 24-GHz WLAN [2], IEEE802.11ad [3], and 24-29 GHz 5G mobile phones [4]. One of essential circuit blocks in the high-frequency wireless systems is a voltage-controlled oscillator (VCO). Various quadrature (4-phase) VCOs are investigated [5-9] for quadrature signal processing [10-17] in the high-performance wireless systems, however, a high-frequency VCO usually needs a very fine CMOS technology and/or a special high-Q inductor process [18-23] for suppressing the phase noise. Thus, their process cost could be considerably high. In this letter, a quadrature VCO using a phase-adjusting architecture for suppressing phase noise is presented. This architecture can be implemented without process-cost increase.

2. Concept of phase adjusting and circuit design

Figure 1 shows the circuit structure and the ac-analysis results of a differential LC VCO. The open-loop gain and phase were calculated with a dummy LC amplifier for adding gate capacitances to an LC parallel resonant circuit. The gain peak locates at the LC resonant frequency of 29.4 GHz, however, the oscillation occurs at 28.7 GHz because of oscillation condition (180° phase delay). The difference between the LC resonant frequency and the oscillation frequency arises from an RC delay in a transistor. The RC delay causes phase delay (-\(\Delta\phi \approx -24^\circ\)) and lowers the oscillation frequency.

Fig. 1. (a) The circuit structure and (b) the gain and phase results of open-loop ac analysis for a differential LC VCO.

The loop gain of VCO should be maximum to suppress its phase noise [24], thus the oscillation frequency should be the LC resonant frequency. Therefore \(\Delta\phi\)-phase shifters at two gate inputs of a differential VCO (depicted in Fig. 2(a)) are thought to be very helpful to adjust phase delay and suppress phase noise. The \(\Delta\phi\)-phase shifter was designed using a weight-average architecture with two capacitors and a source follower circuit as shown in Fig. 2 (b).

Fig. 2. (a) A differential VCO with two phase shifters and (b) A phase shifter circuit using a weight-average architecture with two capacitors and a source follower circuit. \(R\) is a resistor for DC bias.

Figure 3 shows the quadrature VCO circuit using the phase adjusting architecture. Four \(\Delta\phi\)-phase shifters are added to two conventional differential VCOs.
3. Measurement results and discussion

Two conventional differential VCOs [25-29] and the phase-adjusting quadrature VCO were fabricated using TSMC 180-nm CMOS technology (as shown in Fig. 4). The conventional VCOs were realized by just removing the $\Delta \phi$-phase shifters from the phase-adjusting quadrature VCO. The values of $C_1$ and $C_2$ of the $\Delta \phi$-phase shifter were 600 fF and 60 fF, respectively.

The phase noises of the VCOs were measured with various bias voltages, $V_{DD} = 2.2V$ and $V_{ctrl} = 0V$. Figure 5 shows the relation between phase noise at 1-MHz offset frequency and current consumption. The current consumption shows the current of the one conventional differential VCO and that of the phase-adjusting quadrature VCO with four source followers. In terms of bias setting, the quadrature VCO has a larger operation range than that of a conventional VCO. Oscillation frequencies were 28.05 GHz for the conventional VCO and 25.17 GHz for the quadrature VCO. The $\Delta \phi$-phase shifter worked not only as a phase shifter but also as an additional capacitor and thus both of the LC-resonant frequency and the oscillation frequency of the phase-adjusting quadrature VCO were lowered. The ranges of oscillation frequencies were 28.05 to 28.73 GHz for the conventional VCO and 25.17 to 25.73 GHz for the quadrature VCO when $V_{ctrl}$ was changed from 0 to 2.2V. The additional capacitance of the $\Delta \phi$-phase shifter reduced the frequency range by about 10%.

The figure-of-merit (FOM) is defined as [30]:

$$FOM = L(\Delta f) - 20\log(\frac{f_0}{\Delta f}) + 10\log(\frac{P_{DC}}{1mW}).$$  \hspace{1cm} (1)

The best FOMs of the VCOs in Fig. 5 are evaluated as $-175.1\,dBc/Hz$ and $-176.4\,dBc/Hz$ for the conventional VCO and the phase-adjusting quadrature VCO, respectively. A quadrature VCO usually consumes two times larger current than that of a differential VCO, thus the FOM improvement is effectively 4.3 dB. Figure 6 shows the offset-frequency dependence of phase noise at the conditions of minimum phase noise in Fig. 5. Thus in Fig. 6, the current consumptions of the conventional VCOs and the phase-adjusting quadrature VCO were 15.4 mA and 41.2 mA, respectively. The phase-adjusting architecture suppresses phase noise about 9 dB and 6 dB at 100-kHz and 1-MHz offset frequencies, respectively. The 3-dB reduction of phase-noise suppression at 1-MHz offset frequency was probably due to measurement errors as shown in Fig. 6.

Performance comparison with recently published CMOS LC VCOs is illustrated in Table 1. All VCOs are quadrature VCOs. The proposed VCO shows phase-noise performances comparable to the finer-process VCOs.
4. Conclusion

A low-phase-noise 25-GHz quadrature VCO using 180-nm TSMC CMOS is presented. A phase shifter equalizes the oscillation frequency to the LC-resonant frequency, extends operation bias range and suppresses phase noise by 6 to 9 dB compared to a conventional differential VCO.

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References


