CALL FOR PAPERS

ITC-CSCC 2009

The 24th International Technical Conference on Circuits/Systems, Computers and Communications
July 5-8, 2009/ Jeju KAL Hotel, Jeju Island, Korea/ http://www.itc-cscc2009.org

WELCOME TO ITC–CSCC

The 24th International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC 2009) will be held on July 5-8 at Jeju KAL Hotel, Jeju Island, Korea. We would like to invite all scholars and experts around the world to attend the conference to be hosted at the wonderful island of Korea. The meetings at Chiang Mai in 2006, Busan in 2007 and Shimonoseki in 2008 were successfully held in this world leading conference devoted to the advancement of high technologies in Circuits/Systems, Computers and Communications.

TOPICS

The conference is open to researchers from all regions of the world. Participation from Asia Pacific region is particularly encouraged. Proposals for special sessions are welcome. Papers with original works in all aspects of Circuits/Systems, Computers and Communications are invited. Topics include, but not limited to, the followings:

Circuits & Systems
- Adaptive and Learning Systems
- Analog & RF Circuits
- Computer Aided Design
- Display Technology
- Embedded Systems
- Fuzzy and Neural Networks
- Graph Theory & Petri Nets
- Intelligent Systems
- Linear/Nonlinear Systems
- Micro and Nano Systems
- Modern Control
- Signal Processing
- Solid-State Circuits
- Verification & Test Technology
- VLSI Design

Computers
- Artificial Intelligence
- Biocomputing
- Computer Systems & Applications
- Computer Vision
- Database & Data Mining
- Face Detection & Recognition
- Image Coding & Analysis
- Image Processing
- Internet Technology & Applications
- Multimedia Service & Technology
- Object Extraction & Technology
- Operating Systems
- Security
- Software Systems
- Watermarking

Communications
- Antenna & Wave Propagation
- Audio/Speech/Image/Video Signal Processing
- Circuits & Components for Communications
- Communication Signal Processing
- Cooperative Communications
- Cross-layer Design
- IP Networks & QoS
- MIMO & Space-Time Codes
- Mobile & Wireless Communications
- Multimedia Communications
- Network Management & Design
- Optical Communications and Components
- RF Systems
- Ubiquitous Networks
- UWB, Cognitive Radio

SUBMISSION OF PAPERS

Prospective authors are invited to submit abstracts of two (2) pages. Abstracts will be accepted only by electronic submission at http://www.itc-cscc2009.org. Accepted authors should submit maximum four (4) page camera-ready papers. All submissions should be formatted in either MS Word or PDF written in English. If you have any troubles in preparing papers and on-line submission, please contact the conference secretariat.

PROCEEDINGS

All registered participants are provided with a CD including conference proceedings. Moreover, authors of the accepted papers are encouraged to submit full-length manuscripts to IEICE Transactions on Fundamentals or IEEK JSTS (Journal of Semiconductor Technology and Science). Papers passed through the standard editing procedures of the IEICE Transactions or IEEK JSTS will be published. The authors (or their institute) are requested to pay the publication charge for the IEICE when their papers are accepted.

AUTHOR’S SCHEDULE

Deadline for proposal of special session: March 20, 2009
Notification of acceptance: May 15, 2009
Deadline for abstract submission: April 24, 2009
Camera ready manuscripts: June 7, 2009

SPONSORED BY

The Institute of Electronics Engineers of Korea (IEEK)
The Institute of Electronics, Information and Communication Engineers (IEICE), the Engineering Sciences and Electronics Societies, Japan
The Electrical Engineering/Electronics, Computer, Telecommunications and Information Association, Thailand
In cooperation with Technical Committee on Electronic Circuits, The Institute of Electrical Engineers of Japan (IEEJ)
The 2009 International Symposium on Nonlinear Theory and its Applications (NOLTA 2009) will be held at Gateaux Kingdom Sapporo Hotel & Spa Resort, Sapporo, Japan, October 19–21. The objective of the symposium is to provide a forum for exchange of the latest results related to nonlinear theory and its applications. Papers describing original results in all aspects of nonlinear theory and its applications are invited. Topics include, but are not limited to:

- Chaos and Bifurcation
- Oscillation
- Coupled Oscillators
- Communication
- Chaotic Circuits
- Control
- Complex Systems
- Fractals
- Soliton
- Applied Mathematics
- Biomedical Data Processing
- Complex Networks
- Chaotic Neural Networks
- Cellular Neural Networks
- Learning and Memory
- Prediction and Identification
- Image and Signal Processing
- Neuro Dynamics
- Evolutionary Computation
- Optimization
- Fuzzy
- Biocybernetics
- Economics
- Biomedical Engineering
- System Biology
- Circuits and Systems
- Self-Validating Numerics
- Modeling and Simulation
- Large-Scale Networks
- Analog and Digital ICs
- Distributed Networks
- Power Systems
- Robotics
- Optics
- Chemistry
- Physics
- Biomathematics
- Biology
- Chemistry
- Economics
- Biomedical Data Processing
- Biomedical Engineering
- Bioinformatics
- System Biology

ORGANIZER:

Research Society of Nonlinear Theory and its Applications, IEICE

IN COOPERATION WITH:

Technical Group on Nonlinear Problems, IEICE
Technical Group on Circuits and Systems, IEICE
IEEE Circuits and Systems Society

Special Sessions

NOLTA 2009 welcomes proposals for special sessions. Proposals will be evaluated based on the timeliness and the scientific quality of the topic. Notification of acceptance will be sent to the organizers by March 2, 2009. Please submit proposals via email by February 10, 2009 to:

NOLTA 2009 Special Session Proposal E-mail: nolta2009SS@lalsie.ist.hokudai.ac.jp

Student Paper Award

The NOLTA 2009 Symposium Committee will select a small number of papers to receive the Student Paper Award. Candidates are nominated by TPC of NOLTA 2009 before the Symposium. The Candidates are asked to present the paper at the Symposium. The selection is based on the quality, originality, and clarity of the paper and the presentation.

Author’s Schedule

Deadline for Special Session proposals: February 10, 2009
Notification of acceptance for Special Sessions: March 2, 2009
Deadline for submission of full papers for Regular and Special Sessions: April 3, 2009
Notification of acceptance for Regular and Special Session Papers: June 12, 2009
Deadline for submission of all camera-ready papers: July 10, 2009

For more information about NOLTA 2009, please visit the website:

http://lalsie.ist.hokudai.ac.jp/nolta2009/
CALL FOR PAPERS
Submission of papers: April 3, 2009 23:59 JST (UTC + 9hr)

Conference scope
The aim of IWSEC2009 is to contribute to research and development of various security topics: theory and applications of traditional and up-to-date security issues.

Topics include but are not limited to:
- Network and Distributed Systems Security
- Security Issues in Ubiquitous/Pervasive Computing
- Authorization and Access Control
- Software and System Security
- Usable Security
- Privacy Enhancing Technology
- Digital Identity Management
- Digital Forensics
- Biometrics
- Cryptography
- Information Hiding
- Quantum Security
- Secure and Efficient Implementation
- Other Scientific Approaches for Security

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Program Committee co-Chairs
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Paper submission
Submitted papers must be original, unpublished, and not submitted to another conference or journal for consideration of publication. Papers must be written in English and be fully anonymous with no author names, affiliations, acknowledgements or obvious references. They should be at most 16 pages long in total, in standard LNCS format except for author names and affiliations. Papers should be enough intelligible and self-contained without appendices so that PC members can review papers without them. Accepted papers will be presented at the workshop and published in the proceedings. At least one author of each accepted paper is required to register with the workshop and present the paper. The conference proceedings will be published in the Springer's LNCS series.

Important dates
Submission of papers: April 3, 2009 23:59 JST (UTC + 9hr)
Notification to authors: June 8, 2009 JST (UTC + 9hr)
Camera-ready due: July 6, 2009 JST (GMT + 9hr)
Committees

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Katsuyuki Takashima, Mitsubishi Electric Corporation, Japan.
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Maki Yoshida, Osaka University, Japan.
Hirosi Yoshuira, University of Electro-Communications, Japan.
Fangguo Zhang, Sun Yat-sen University, China.
Jianying Zhou, Institute for Infocomm Research, Singapore.
Alf Zugenmaier, DOCOMO Euro-Labs, Germany.

Contact to info2009@iwsec.org
December 7-9, 2009
Kanazawa Excel Hotel Tokyu, Kanazawa, Japan

First Call for Papers
Integration of Multimedia Signal Processing and Communication Systems for the Future

The 2009 International Symposium on Intelligent Signal Processing and Communication Systems will be held in Kanazawa, Japan. The ISPACS presents every possibility on new information technologies. The ISPACS 2009 will include many regular sessions on the topics listed below and some special sessions on emerging intelligent signal processing and communication system topics. The symposium will be organized into parallel sessions. Prospective authors are invited to submit their papers reporting original work as well as tutorial overviews in all areas of signal processing and communication systems. The topics for regular sessions include, but are not limited to the following fields:

1. Communication Systems
   1.1 Radio Propagation and Channel Modeling
   1.2 Communication Theory
   1.3 Antenna and Propagation
   1.4 Wideband Communications
   1.5 Wireless Systems
   1.6 Intelligent Communication Systems and Network Protocols
   1.7 Others

2. Multimedia and Systems
   2.1 Speech Processing and Coder
   2.2 Video Processing and Coder
   2.3 Video and Multimedia Technology & Communications
   2.4 Audio/Acoustic Signal Processing
   2.5 Multimedia Processing for e-Learning
   2.6 Others

3. Signal Processing
   3.1 Digital Filters and Filter Banks
   3.2 Wavelets and Multirate Signal Processing
   3.3 Adaptive, Non-linear and Multidimensional Signal Processing
   3.4 Fast Computation for Signal Processing and Communication Systems
   3.5 Radar, Antennas and Mobile Signal Processing
   3.6 Intelligent Signal Processing for Communications & Systems
   3.7 Security Signal Processing
   3.8 Optical Signal Processing
   3.9 Noise Control
   3.10 Others

4. VLSI
   4.1 Analog and Digital lcs for Communications
   4.2 Low Power Design & VLSI Physical Synthesis
   4.3 Modeling, Simulation and CAD Tools
   4.4 VLSI Architecture for Signal Processing
   4.5 Others

5. Circuits and Systems
   5.1 Analog Circuits, Filters and Data Conversion
   5.2 Analog and Mixed Signal Processing
   5.3 Numerical Methods and Circuit Simulation
   5.4 Circuits and Systems for Communications
   5.5 Neural Networks and Fuzzy Logic Processing
   5.6 Others

6. Emerging Technologies in Signal Processing and Communications

Authors are invited to submit a full paper according to posted guidelines. Only electronic submissions will be accepted via the Web at:

http://splaite.iet.itac.jp/ispacs2009/

Authors are expected to present their papers at the Symposium upon acceptance and presenting authors are required to register for the Symposium.

Author’s Schedule:
Deadline for Special Session Proposals: May 31, 2009
Deadline for Submission of Full Paper: June 15, 2009
Notification of Acceptance: September 1, 2009
Deadline for Submission of Camera Ready Paper: October 1, 2009

Check the symposium website for up-to-date information:

http://splaite.iet.itac.jp/ispacs2009/