Performance and Design of an L-C-L Converter for Voltage Regulator Type Applications

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In resonant converters, lagging power factor operation is preferred. Also, if the converter switches are operated at constant frequency, or during the operating load range, the change in frequency is kept minimum, then the switch as well as the converter components can be best utilized. The paper investigates a three-element L-C-L converter topology to operate with minimum change in operating frequency. The analysis and performance are reported for lagging power factor mode of operation. The analysis yields easy-to-use closed-form equations, which can be used to design the converter. Design criteria for voltage regulator type applications are discussed. The usefulness of the design methodology is proved by prototype experiments.

Keywords: Resonant converter, High-frequency, Voltage regulator, Converter design

1. Introduction:
Resonant converters[1-17] are smaller in size and because of ZVS and/or ZCS operation, the switching losses are drastically reduced. Additional snubber components and heat sink are no longer required. Also selection of operating frequency beyond audible range enables noise-free operation.

The two most popular topology for DC/DC converters are Series Resonant Series Loaded (normally called as Series Resonant Converter or SRC) and Series Resonant Parallel Loaded (normally called as Parallel Resonant Converter or PRC) [1-3]. The part-load efficiency of the SRC is good but the no-load regulation is the poorest. In fact the no load voltage cannot be regulated by frequency regulation. This makes SRC unsuitable for voltage regulator type applications, when controlled by frequency adjustment. On the other hand PRC provides excellent regulation at no-load, thus became a strong candidate for voltage regulator type applications[7].

The PRC has the advantage of minimum components count but the major problem is that of poor part-load efficiency. Efforts to improve upon the basic PRC structure unearthed different multi elements Series-Parallel topologies[3,7-10,13-17]. Extensive investigations have been conducted on this topologies but interestingly in many cases efforts towards providing guide line for better design in specific applications are missing in literature. The present paper deals with the L-C-L topology[8] for voltage regulator applications. The topology is explained first followed by design criteria, converter analysis, design, performance and results from experiments.

2. Converter Description:
Fig.1 shows the topology considered for the present study. The load is connected across $C_1$ in series with $L_2$. Thus the converter preserves the basic parallel structure and at no-load behaves exactly in the same way. Although the converter in its present form was reported earlier[8], but different aspects of better design for voltage regulator type applications are missing, which is pursued in the present paper. It has been shown later in this report that a proper selection of $L_2$ provides load-independent characteristics. Although L-C-C converter is topologically more attractive for it requires only one inductor, which is bigger in size compared to a capacitor, however, this should not be a basic guideline for the selection of converter topology, because capacitor in series with the load should be able to carry higher current and inductor size reduces with the selection of higher operating frequency. Although full bridge inverter at the input and normal diode bridge at the output are used but, depending on application requirement, half-bridge or totem pole inverter may be used. Also use of transformer at the output side of the L-C-L resonant circuit provides option for use of half-wave rectifier or bridge rectifier. Fig.2 shows several options for transformer coupled converters. The winding leakage...
3. Design Criteria:

A design should be judged or evaluated on the basis of the selected criteria. Converter design depends on specific application, control methodology and on the type of the converter itself. For the resonant converters, it has been well explained that operation above resonance (i.e. at operating frequency greater than resonating frequency) eliminates snubber requirements[7] and thus now-a-days have become a standard choice for many applications. Also, minimum deviation in operating frequency to control the converter output voltage ensures better utilization of the switches and converter components[11-12,14]. The third aspect is design for better part-load efficiency. So the present study on voltage regulator type applications is based on the following criteria,

(i) Operation in above resonance or lagging power factor mode,
(ii) Operation at constant frequency or under minimum variation in frequency,
(iii) Better part-load efficiency.

4. Converter Analysis:

The fundamental frequency approach is followed to yield closed-form equations and to facilitate converter design. The different symbols used in this paper are explained below. Only $E_{IN}$ and $E_{O}$ are DC, all other symbols for voltages and currents expressed in uppercase letters, indicate AC rms quantities.

$E_{IN}$ = Input DC Voltage,
$E_{O}$ = Output DC Voltage,
$V_{IN}$ = Rms magnitude of the fundamental component of the inverter output voltage,
$V_{O}$ = Rms magnitude of the fundamental component of the voltage at the input of the rectifier bridge,

Converter AC Voltage Gain ($M$) may be defined as,

$$M = \frac{V_{O}}{V_{IN}}$$

$M_{NL}$ = Converter AC voltage gain at no-load,
$I_{NL}$ = Current through $L_1$ and $C_1$ at no-load (No-load current),
$I_{PL}$ = Current through $L_2$ at rated load, $R$ = Load resistance,
$R_{PL}$ = Rated load resistance,
$P_{OPL}$ = Rated power output,
$R_{ac}$ = Reflected equivalent resistance on the AC side,
$a$ = Transformer turns ratio,
$V_{TH}$ = Thevenin Voltage,
$Z_{TH}$ = Thevenin impedance,

$$\alpha = 1 - \left( \frac{\omega}{\omega_0} \right)^2$$

$$\beta = \frac{I_{NL}}{I_{PL}}$$

$$\gamma = \frac{L_2}{L_1}$$

$C_2 = aC_1$

$$Q_1 = \frac{r}{\omega_0 L_1}$$

$$Q_2 = \frac{\omega_0 L_2}{R}$$

$$denom = \sqrt{\left( \frac{\omega}{\omega_0} \right)^2 Q_1^2 + \left( \frac{\omega}{\omega_0} - 1 \right)^2}$$

The inverter converts the input DC voltage ($E_{IN}$) to a square wave voltage ($v$) expressed as,

$$v = E_{IN} \text{ for } 0 < \omega t \leq \pi$$

$$-E_{IN} \text{ for } \pi < \omega t \leq 2\pi \hspace{1cm} (1)$$
Fig. 3 shows the equivalent AC network, where $r$ represents the sum of switch on-resistance, the resistance of the inductor $L_1$ and source resistance (if any). $V_{in}$, $R_w$, and $V_o$ may be expressed as below,

\[ V_{in} = \frac{2\sqrt{2}}{\pi} (E_{in} - 2V_{sw}) \]  

\[ R_w = \frac{8}{\pi^2} R \]  

\[ V_o = \frac{2\sqrt{2}}{\pi} (E_o + 2V_d) \]

Where $V_{sw}$ and $V_d$ are the on-state voltage drop in inverter switch and forward voltage drop in diode respectively.

Thus Converter AC Voltage Gain may be expressed as,

\[ M = \frac{V_o}{V_{in}} = \frac{E_o + 2V_d}{E_{in} - 2V_{sw}} \]

It is evident from (5) that the Converter DC Voltage Gain (i.e. $E_0/E_{IN}$) will be same to that of Converter AC Voltage Gain if the drops in inverter switches and output diodes are neglected. However, knowing the switch and diode drops, Converter DC Voltage Gain can easily be calculated from (5).

Fig. 4 shows the Thevenin’s equivalent circuit of Fig. 3, where $V_{TH}$ and $Z_{TH}$ are expressed as

\[ V_{TH} = \frac{V_{IN}}{denom} \]  

\[ Z_{TH} = R_{TH} + jX_{TH} \]

Where

\[ R_{TH} = \frac{r}{(denom)^2} \]

\[ X_{TH} = \frac{-\omega L_1 \left[ \left( \frac{\omega}{\omega_o} \right)^2 - 1 \right] + Q_1^2}{(denom)^2} \]

As $r << \omega L_1$, so neglecting $r$ and $Q_1$, equations 6, 8 and 9 take the following form,

\[ V_{TH} = \frac{V_{IN}}{\left[ \left( \frac{\omega}{\omega_o} \right)^2 - 1 \right]} \]

\[ R_{TH} = 0 \]

For $\omega > \omega_o$, $X_{TH}$ is negative thus may also be expressed in terms of capacitance as shown below

\[ X_{TH} = \frac{-1}{\left[ 1 - \left( \frac{\omega_o}{\omega} \right)^2 \right] \omega C_1} = -1 \frac{1}{\omega C_2} \]

Thus the operation above resonance created a capacitance in series with the load. Taking into account that $V_{TH} = V_{ML}$, the simplified equivalent circuit is shown in Fig. 5. From (10), the no-load gain may be expressed as

\[ M_{ML} = \frac{1}{\left[ \left( \frac{\omega}{\omega_o} \right)^2 - 1 \right]} \]

The no-load current (flowing through $L_1$ and $C_1$ when load current is zero) may be expressed as,

\[ I_{ML} = \frac{V_{IN}}{\omega_0 L_1 \left[ Q_1^2 + \left( \frac{\omega}{\omega_o} \right)^2 - \left( \frac{\omega_o}{\omega} \right)^2 \right]} \]

Neglecting $Q_1$, the expression of $I_1$ becomes,

\[ L_1 = \frac{V_{IN}}{\omega_0 \beta \left( \frac{\omega}{\omega_o} \right) \left( \frac{\omega_o}{\omega} \right)} \]
The AC voltage gain may be expressed as

\[ M = \frac{1}{\left(\text{denom}\right)^2} \left[ 1 + \frac{\pi^4 Q_2^2}{64} \left( \frac{\omega}{\omega_0} \right)^2 \left( \frac{\omega}{\omega_0} - \frac{1}{\alpha} \left( \frac{\omega_0}{\omega} \right) \right) \right] \]

Again neglecting \( Q_0 \), the equation simplifies to,

\[ M = \frac{1}{\left[ \left( \frac{\omega}{\omega_0} \right)^2 - 1 \right]^2} \left[ 1 + \frac{\pi^4 Q_2^2}{64} \left( \frac{\omega}{\omega_0} \right)^2 \left( \frac{\omega}{\omega_0} - \frac{1}{\alpha} \left( \frac{\omega_0}{\omega} \right) \right) \right] \]

Thus \( M \) will become independent of load variation if

\[ \frac{\pi^4}{64} Q_2^2 \left( \frac{\omega}{\omega_0} \right)^2 \left( \frac{\omega}{\omega_0} - \frac{1}{\alpha} \left( \frac{\omega_0}{\omega} \right) \right)^2 = 0 \]

Simplification of (19) results in

\[ \gamma = \frac{L_2}{L_1} = \frac{1}{\left[ \left( \frac{\omega}{\omega_0} \right)^2 - 1 \right]} \]

The currents through the converter elements \( L_1 \), \( C_1 \) and \( L_2 \) may be expressed as,

\[ I_{C1} = V_o \omega C_1 \left[ 1 + \frac{\pi^4}{64} Q_2^2 \left( \frac{\omega}{\omega_0} \right)^2 \left( \frac{\omega}{\omega_0} - \frac{1}{\alpha} \left( \frac{\omega_0}{\omega} \right) \right) \right] \]

\[ I_{L2} = \frac{\pi^2}{8} V_o \frac{Q_2}{\omega \alpha L_1} \]

The voltages across the different elements may be expressed as,

\[ \frac{V_{in}}{V_{in}} = M \left[ \frac{\omega}{\omega_0} \right]^2 \left( \frac{\omega}{\omega_0} - \frac{1}{\alpha} \left( \frac{\omega_0}{\omega} \right) \right)^2 \]

\[ \frac{V_{C1}}{V_{in}} = \frac{\pi^2}{8} \gamma M Q_2 \left( \frac{\omega}{\omega_0} \right)^2 \]

The expression of the total inductance is

\[ L_T = L_1 + L_2 = L_1 (1 + \gamma) \]

In terms of \( M \), equation 27 may be expressed as,

\[ L_T = \frac{8R_{in} (1 + M)^2}{\pi^2 \omega \beta M} \]

Thus for a chosen operating frequency and \( \beta \), \( L_T \) becomes minimum when

\[ \frac{dL_T}{dM} = 0 \]

which yields the value of \( M \) to be 1. The corresponding \( \omega \omega_0 \) is \( \sqrt{2} \). The total minimum inductance which occurs when \( M=1 \) (with corresponding \( \gamma=1 \)), may be expressed as,

\[ L_{T(MIN)} = \frac{32R_{in}}{\omega \beta \pi^2} \]

As the currents through \( L_1 \) and \( L_2 \) are different, so minimum total inductance magnitude does not necessarily yield minimum size. It is well known that the inductor size is proportional to its energy storage capacity. Thus for a specific core material and for a desired temperature rise and insulation type, the combined size of the inductor is minimum when

\[ \frac{d}{dM} \left[ \frac{1}{2} L_T \left( L_1^2 + L_2^2 \right) \right] = 0 \]
Which when simplified becomes,

\[ 2M^3 + 2M^2 - \beta^2 = 0 \]  

(32)

Thus for a desired \( M \) (and also corresponding \( \gamma \)) can be found from (32) to make the total inductor size minimum.

In practice input and output voltage being specified, the designer has no chance to manipulate \( M \), however for transformer isolated converters, adjusting transformer turns ratio, a better design may be achieved. This aspect is discussed briefly, later in section 6.2.

5. Performance Characteristics:

For a voltage regulator, from load point of view, the extreme conditions are no-load and short-circuit. A PRC structure is topologically short-circuit proof. But the addition of \( L_2 \) in the PRC structure (resulting in L-C-L topology), introduces series characteristics and when \( \gamma \) is selected according to (20), the converter operates at resonance and loses the inherent short-circuit protection feature. However this is not a big problem, because, the short-circuit protection can easily be included in the control circuitry, which when short-circuit occurs, makes the frequency much higher, thus operating safely away from the resonant point.

To study the performance of the converter, different curves are plotted in a systematic manner. Fig.6 shows the variation of AC voltage gain at no-load with the change in frequency ratio. Depending on the output voltage, the frequency ratio may be selected from this curve. At no-load condition as \( L_2 \) has no part to play, so the L-C-L converter and the PRC behave identically at no-load. Fig.7 and 8 show the converter AC voltage gain vs. frequency ratio characteristics at different values of \( \gamma \), for two different loads, corresponding to \( Q_2 = 5 \) and 1 respectively. It is obvious that with lower \( Q_2 \), the \( M \) vs. \( \omega / \omega_0 \) curve will approach the no-load characteristics. However due to the presence of \( L_2 \), there is a marked difference in the load characteristics compared to the normal PRC, which is clearly reflected in Fig.8. Instead of actual magnitude, \( L_2 \) is taken into account by the ratio \( \gamma \), to facilitate design and for a better understanding of the network. For a definite load, increase in the magnitude of \( \gamma \), moves the resonance peak towards lower magnitude of the \( \omega / \omega_0 \). Also a comparison of Fig.7 and 8 reflects that increase in \( Q_2 \) (or load) also makes the \( M \) vs. \( \omega / \omega_0 \) characteristics more sharp (frequency sensitive). In Figs. 7 and 8, the case for \( \gamma = 0 \) is also included for comparison with the PRC. Selection of \( \gamma \) and operating frequency is very important for a better performance of the converter.

Fig.9 shows the converter AC voltage gain vs. \( \omega / \omega_0 \) characteristics for different loads at a specific \( \gamma \).

The frequency of operation at which load independency occurs, is shown in Fig.9 as the point "A". From Fig.9 it is clear that at higher loads, operating frequency higher than \( \omega_A \), ensures lagging power factor operation, and if the frequency becomes less than \( \omega_A \), then depending on the magnitude of the load, the converter may enter in the leading power factor mode. Fig.10 shows the load characteristics (i.e. \( M \) vs. \( Q_2 \)) for different values of \( \gamma \). As the converter gain is considered to be 1, so when \( \gamma = 1 \) (i.e. \( M = \gamma \)), the characteristic shows load independent feature. At all other values of \( \gamma \) the converter output voltage undergoes regulation.
L-C-L Converter for Voltage Regulator Type Applications

For a series resonant type converter (SRC), efficiency becomes best if the converter is operated at resonance (i.e. at resonating frequency). Because at resonance the reactive component of the current is minimum (zero), thus for the same power transfer conduction loss is minimized. A PRC can never be operated at resonance (i.e. at resonating frequency) [7], whereas the L-C-L converter with a proper selection of operating frequency and \( \gamma \) may be operated at resonance corresponding to load higher than the no-load. If the no-load current is restricted to a lower magnitude, the part-load efficiency of such converter will be definitely better compared to a PRC. Efficiency computations need device and components details and hence not pursued here. However, it can easily be calculated by computing the currents \( I_{L1} \), \( I_{C1} \) and \( I_{L2} \) using (21), (22) and (23) respectively and then the losses associated with the converter components and devices.

6. Converter Design

Converter design involves determination of the device voltage/current ratings, selection of components magnitude and operating frequency along with the details of the control circuitry. However this paper deals only with the selection of components (i.e. \( L_1 \), \( C_1 \) and \( L_2 \)) magnitude for better performance. The rest of the design will follow standard procedure [3].

If there is no input voltage variation, the converter may be designed optimally to operate at constant frequency by selecting a proper \( \gamma \). But voltage regulator type applications demand constant output voltage under the circumstances of input voltage and output load variation (within specified range). So operating frequency and \( \gamma \) should be selected corresponding to the highest \( M \) (i.e. for the case of lowest possible input voltage). For lower \( M \) (i.e. lower than the “highest M” for which the converter is designed), output voltage may be controlled by controlling (slightly increasing) the frequency.

6.1 Design for transformer-less converter:

The combination \( L_1 \), \( C_1 \) and \( \omega \) decides \( I_{SN} \) and the output voltage at no-load. \( L_2 \) decides the load performance and if \( \gamma \approx M \) then the converter exhibits load independent features. \( R_{FL} \) may be calculated as

\[
R_{FL} = \frac{F_0}{P_{out(FL)}}
\]

If the converter is supposed to deliver power more than the rated (may be for short interval), then, \( R_{FL} \) should be calculated considering the specified maximum power output from the converter, and if by any chance during operation the converter power exceeds the specified maximum value, then the base/gate drive to the converter devices must be withdrawn to avoid entering into the leading power factor mode.

For a specified \( E_o \) and \( E_{IN} \), \( M \) may be computed using (5). If input voltage undergoes variation, \( M \) should be calculated corresponding to the minimum \( E_{IN} \). Assuming \( \beta \) and operating frequency (i.e. \( \omega \)), \( L_1 \) may be calculated using the following equation (derived from (16)),

\[
L_1 = \frac{8R_{FL}(1+M)}{\pi \omega^2 \beta M}
\]

\( C_1 \) may be calculated from the following equation,

\[
C_1 = \frac{1+M}{\omega^2 ML_1}
\]

\( L_2 \) may be calculated using (20).

Thus it is understood that the designer has to select \( \beta \) and \( \omega \). As higher \( \omega \) allows miniaturization of the network components, so operating frequency selection is decided by the upper allowable working frequency limit of the converter devices. A lower \( \beta \) yields better part-load efficiency but demands increased inductance magnitude, thus increasing the size of the converter. So a trade-off is necessary in between converter allowable size (with the associated increase in cost) and efficiency.

The converter components of an IGBT based 48 volts (±10%) input, 24 volts output (i.e. Bulk Type), transformer less converter for a maximum load current of 5A are calculated as below. Operating frequency has been selected to be 30 kHz. The IGBT drop and diode drop are considered to be 2.5 Volts and 2 volts respectively. For \( \beta = 0.1 \), \( L_1 = 488 \mu H \), \( C_1 = 0.136 \mu F \), \( L_2 = 357.6 \mu H \), For \( \beta = 0.5 \), \( L_1 = 97.6 \mu H \), \( C_1 = 0.68 \mu F \), \( L_2 = 71.5 \mu H \).
The converter components of a 24 volts (±10%) input and 48 volts output (i.e., Boost Type), 2.5 A, for the same operating frequency are calculated as follows:

For $\beta = 0.1$, $L_1 = 1.315$ mH, $C_1 = 0.17$ µF, $L_2 = 443.6$ µH.
For $\beta = 0.5$, $L_1 = 263$ µH, $C_1 = 0.17$ µF, $L_2 = 43.6$ µH.

6.2 Design for transformer-isolated converter:

In case of transformer-isolated converters, the designer has a little more flexibility. The transformer turns ratio may be properly designed to have control over the ratio $a\omega_0$. However, transformer non-linearities will also have a part to play in the converter performance, which needs further investigations. Assuming ideal transformer and considering the circuit topology of Fig. 2a, equation 32 takes the following form:

$$2(aM)^2 + 2(aM)^2 - \beta^2 = 0$$

(36)

So, to design a converter with minimum inductor size "aM" can be found from (36), assuming $\beta$. Thus $E_{in}$ and $E_o$ being specified, the turns ratio "a", can easily be calculated. Fig. 11 shows a plot of (36) (only the real positive root of "aM" vs. $\beta$), which shows a lower $\beta$ requires lower "aM".

It is sufficient to design the transformer considering only the active power equal to the rated output power of the converter. This is because with proper design, the output AC current of the inverter at rated load, will become in phase with the inverter output voltage. Thus neglecting the effect of the magnetizing current of the transformer, the transformer voltages and currents become in phase at rated load. So in the present case, optimization of the combined inductors size will lead to a total size-optimization of the transformer-isolated converter.

It is mentioned earlier that with proper design and no input voltage variation, the required variation of operating frequency with the change in load is minimum (ideally zero). However, for voltage regulator applications, usually the input voltage also changes. As for lower magnitude of $y$, the M vs. $\omega_0$ characteristic becomes more flat, so, the required variation in operating frequency with the change in input voltage increases with decrease in $y$. Thus a careful selection of "a" and $y=aM$ are required for better performance. Also, a compromise between converter size and performance may be essential in case of lower $y$.

7. Experimental Results:

For experiments an IGBT based converter prototype is fabricated. Rather than designing a converter assuming operating frequency and $\beta$, and then calculating the inductances and capacitance magnitudes, experiments are conducted in the laboratory for different available inductances of 306 µH, 240 µH, 426 µH and capacitor of 0.2 µF. Two different cases, one for $\gamma<1$ (i.e., $L_2<L_1$) and the other for $\gamma>1$ (i.e., $L_2>L_1$) are considered. For both the cases output voltage is decided to be kept constant at 24 volts. For the first case with $L_2=306$ µH, $L_1=240$ µH and $C_1=0.2$ µF, $y$ is found to be 0.78. The resonant frequency at no-load ($f_0$) being 20.3 kHz, operating frequency ($f$) is calculated to be 30.7 kHz and assuming IGBT drop of 2.5 volts and diode drop of 2 volts, the corresponding $E_{in}$ is calculated to be 40.7 volts. Experimental results are shown in Fig. 12. Initially, to find the operating frequency (i.e., the frequency at which resonance occurs under full-load condition), load current is adjusted to 2A at $E_{in}=24$ volts. Frequency is adjusted for resonance to occur and found to be 29.5 kHz, which is close to the calculated magnitude (30.7 kHz). The input voltage is found to be 41 volts (instead of 40.7 volts). Also it has been found that keeping the $E_{in}$ constant, lowering of load from full load (2A)(Fig. 12a) to half load (1A)(Fig. 12b) requires a frequency increment of 1.6 kHz and from full load to 25% load (0.5A)(Fig. 12c) needs a frequency increment of 2.4 kHz. Although ideally frequency should remain same with the change in load, but in practice it is experienced that a slight increment in frequency is required. The change in frequency increases with the variation in load. However as the corresponding current through the converter devices also decreases with the increase in frequency and a good amount of output voltage controllability is achieved by frequency variation, so from the point of view of device loss and safety, this seems not a big problem. Fig. 12 shows experimental waveforms for different load conditions.

For the second case $L_2=306$ µH, $L_1=426$ µH and $C_1=0.2$ µF are selected. These give a $\gamma$ of 1.39. The no-load resonant frequency $f_0$ remains same at 20.3 kHz, the operating frequency becomes 26.6 kHz and again considering IGBT drop of 2.5 volts and diode drop of 2 volts, the required $E_{in}$ is found to be 25.1 volts for $E_{in}=24$ volts. Experimental waveforms are shown in Fig. 13, which indicates $f=26.6$ kHz (instead of 26.6 kHz) and $E_{in}=26$ volts (instead of 25.1 volts). Output load is reduced from full load (2A)(Fig. 13a) to 25% load (0.5 A)(Fig. 13c) to 5% load (0.5 A)(Fig. 13e) needing a frequency increment of only 1.7 kHz.

The M vs. $\omega_0$ Characteristics have been obtained from analysis for the two cases corresponding to $\gamma=0.78$ (with $L_2=306$ µH, $L_1=240$ µH and $C_1=0.2$ µF) and $\gamma=1.39$ (with $L_1=306$ µH, $L_2=426$ µH and $C_1=0.2$ µF) as discussed above and shown in Fig. 14 and Fig. 15 respectively. Experiments have been conducted to verify the analytical results. Experimental points have been shown in Fig. 14 and Fig. 15. A fairly good agreement between analytical and experimental result has been obtained.

It is important to note that at absolute no-load, the output filter capacitor is charged corresponding to the peak magnitude of the voltage across $C_1$, rather than by the average of the current flowing through $L_0$ as in the loaded condition, thus increasing the voltage gain and requiring a wider increase in frequency as shown in Fig. 12d and 13d.

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Fig. 12. "W" vs. $\omega_0$.
This paper has presented the design guideline of an L-C-L converter for voltage regulator type applications. Only lagging power factor mode is considered because it does not require any device snubber. The performance and analysis show the existence of a unique $\gamma = \frac{L_2}{L_1}$ for constant frequency operation. Design procedure to improve part-load efficiency is also discussed. It has been shown that by a judicious choice of $\beta$ and $\gamma$, a better design results.

**Fig. 12**
Experimental Waveforms for $\gamma < 1$
(Volt Scale=50 Volts/div. Amp Scale=5A/div. Time Scale=10µs/div.)

**Fig. 13**
Experimental Waveforms for $\gamma > 1$
(Volt Scale=50 Volts/div. Amp Scale=5A/div. Time Scale=10µs/div.)

**Fig. 14. M vs. $\omega_0$ Characteristic for $\gamma=0.78$
$L_1=306 \mu H, L_2=240 \mu H, C_1=0.2 \mu F$**

**Fig. 15. M vs. $\omega_0$ Characteristic for $\gamma=1.39$
$L_1=306 \mu H, L_2=426 \mu H, C_1=0.2 \mu F$**

**9. Conclusions:**
The analysis also shows the way to find trade-off solutions for conflicting design requirements. The proposed design technique is proved by experiments. The results from experiments are found to be sufficiently close to that calculated from the analysis, the difference being due to the assumptions of ideal device and components. The results will definitely help in designing an L-C-L converter with better performance in case of voltage regulator type applications.
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