Partial shading on a photovoltaic (PV) panel consisting of multiple substrings is known to trigger a significant reduction in energy yield and the occurrence of multiple maximum power points (MPPs), including one global and multiple local MPPs. Uneven irradiance on curved PV panels, such as flexible panels and solar roofs of electric vehicles, also causes characteristic mismatch, triggering the same issues. Although various types of differential power processing (DPP) converters have been proposed to preclude the characteristic mismatch issues, the DPP converter is separately required in addition to an existing boost converter for panel control, resulting in increased system complexity, cost, and volume. This study proposes a novel integrated converter that realizes system simplification and circuit miniaturization by integrating a PWM boost and a DPP converter into a single unit. Laboratory and field tests were performed emulating partial shading and characteristic mismatch conditions. The proposed converter eliminated local MPPs and increased maximum power while boosting the panel voltage, demonstrating the efficacy of the proposed integrated converter.

Keywords: differential power processing converter, integrated converter, uneven irradiance, photovoltaic panel, PWM boost converter

1. Introduction

Partial shading on photovoltaic (PV) panels comprising multiple substrings connected in series causes a significant mismatch in substring characteristics, as shown in Fig. 1(a). The shaded substring, which is less capable of producing current, is bypassed by a parallel-connected bypass diode, and its voltage becomes subzero. The shaded substring no longer contributes to power generation, resulting in a significant reduction in the energy yield of the panel as a whole. Furthermore, P–V characteristics of the panel exhibit multiple maximum power point (MPPs), including one global and multiple local MPPs, as shown in Fig. 1(b). In general, an MPP tracking control is performed to maximize energy yield in PV systems, but the panel might operate at a local MPP that is a suboptimal point producing less power than the global MPP.

These negative influences originate from the electrical characteristic mismatch of substrings. Similar characteristic mismatch situations likely happen in various applications. In PV panels mounted on electric vehicles’ roof, for example, substring characteristics are always mismatched to some extent due to uneven irradiance on the curved roof, as illustrated in Fig. 1(c). Another example is a flexible PV panel that is used on curved surfaces.

To cope with such problems, various kinds of differential power processing (DPP) converters, also known as voltage equalizers, have been proposed. DPP converters transfer a fraction of generated power from unshaded substrings to shaded ones so that all substrings operate at the same voltage or even at each MPP. DPP converters can be categorized into several architectures, depending on power redistribution scenarios, as listed in Fig. 2.

The most straightforward architecture is the adjacent substring-to-substring topologies based on non-isolated bidirectional converters, such as PWM converters and switched capacitor converters. With these topologies, adjacent two substrings exchange power under partial shading conditions. The panel-to-substring DPP converters are often employed thanks to their topology popularity, but each DPP converter requires at least two switches and one flyback transformer. In addition, the number of DPP converters necessary is proportional to the number of substrings, hence resulting in increased system complexity and cost.

Meanwhile, the panel-to-substring DPP converter architecture can dramatically reduce not only the switch count but also the number of DPP converters necessary in the system. These panel-to-substring DPP converters are based on single-input–multi-output converters, such as multi-winding flyback converter, multi-stacked buck-boost converter, and LLC resonant voltage multiplier (VM). The conventional LLC-VM for three substrings are shown in Fig. 3, as a representative topology. There are only two switches, regardless of the number of substrings. This DPP converter redistributes power from the panel to shaded
substrings having the lowest voltage even without feedback control. All substring voltages or characteristics are nearly unified, and the negative influences of partial shading are automatically precluded.

Meanwhile, a boost converter is generally indispensable to bridge the voltage gap between PV panels and loads. Since a DPP converter and a boost converter are separately required, conventional PV systems are prone to complexity, as shown in Fig. 4(a). If the boost converter and DPP converter were to be integrated into a single unit with reducing circuit components, the PV system and converter circuit would be even simpler and less expensive.

This paper proposes a novel PWM boost converter integrating the DPP converter. A PWM boost converter and LLC-VM-based DPP converter are integrated into a single unit with sharing switches, hence reducing the system and circuit complexity—the notional system is shown in Fig. 4(b). Similar to the conventional LLC-VM, the DPP converter nearly unifies substring voltages or characteristics without feedback control.

This paper is organized as follows. Section 2 describes the proposed integrated converter and its major features, followed by the operation analysis in Section 3. Section 4 presents experimental results of laboratory and field testing for four substrings connected in series under partial shading and characteristic mismatched conditions, respectively.

2. Proposed Integrated PWM Boost Converter

2.1 Circuit Description

The proposed integrated PWM boost converter for three substrings is shown in Fig. 5. The proposed converter is derived from the combination of the conventional LLC-VM-based DPP converter and the PWM boost converter containing a flying capacitor $C_f$. These converters are integrated by sharing switches of $Q_L$ and $Q_H$. The key concept of the integration is that the LLC-VM is driven by a square wave voltage generated at the switching node or the voltage across $Q_L$, $V_{QL}$.

One might consider that a traditional boost converter could be a foundation circuit for the integrated converter, but not recommended for the integration. A peak-to-peak voltage
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**Fig. 5.** Proposed PWM boost converter integrating LLC-VM-based DPP converter for three substrings

**Fig. 6.** Boost converter topologies and their square wave voltage generated at switching node. (a) Traditional boost converter. (b) Boost converter with a flying capacitor generated at a switching node in the traditional boost converter is equal to an output voltage \( V_{out} = V_{panel} / (1 - d) \) (where \( d \) is the duty cycle), as shown in Fig. 6(a). Since the LLC-VM is driven by \( v_{QL} \) with a peak-to-peak voltage of \( V_{out} \), output voltages of the LLC-VM (i.e., voltages of \( C_{pv1} - C_{pv3} \)) vary with \( d \). In such a case, the LLC-VM would provide insufficient currents to shaded substrings or unnecessarily supply excessive currents to unshaded substrings. With the boost converter for the proposed integrated converter [see Fig. 6(b)], on the other hand, a peak-to-peak voltage at a switching node is always \( V_{panel} \) and is independent on \( V_{out} \). Hence, similar to the conventional LLC-VM, the LLC-VM is driven by \( v_{QL} \) with a peak-to-peak voltage \( V_{panel} \), realizing the automatic preclusion of characteristic mismatch issues without feedback control.

**2.2 Features** The conventional PV system using a DPP converter and traditional PWM boost converter separately [see Fig. 4(a)] requires three switches in total; two and one switches in the DPP and boost converters, respectively. The proposed integrated converter, on the other hand, can reduce the total switch count as low as two, in addition to the reduced system complexity by integrating two converters (i.e., the boost converter and DPP converter) into a single unit.

The flying capacitor \( C_f \) contributes to the miniaturization of the inductor \( L \) compared with that in a traditional PWM boost converter. \( C_f \) not only reduces the applied voltage across \( L \) but also processes a fraction of the total output power, as will be detailed in Section 3.3. The added \( C_f \) has a very tiny impact on the circuit volume because an energy density of discrete capacitors is generally within three orders of magnitude over that of similarly scaled inductors (32)-(33).

In summary, the proposed integrated converter realizes the system simplification and circuit miniaturization by integrating two converters into a single unit while achieving the reduction of the switch count and inductor miniaturization.

A narrower boost conversion range should be cited as a drawback. The boost conversion ratio of the proposed converter is theoretically \( 1 + d \) and is rather narrower than that of traditional boost converter [i.e., \( 1 / (1 - d) \)]. The conversion range can be extended by employing a tapped inductor (TI) in an extended topology, as will be introduced in the following subsection.

Another drawback is that the performance of the DPP converter (i.e., the LLC-VM) in the integrated converter cannot be optimized because of the integration. An optimal \( d \) of the LLC-VM is 50%, but \( d \) is determined by the voltage conversion ratio of the PWM boost converter. Hence, the proposed integrated converter is considered best suitable for applications where the system simplification and cost reduction are prioritized over performance optimization.

**2.3 Extended Topologies** Two magnetic components (i.e., the inductor and transformer) can be integrated by properly utilizing a leakage and magnetizing inductances of a transformer, further miniaturizing the circuit, as shown in Fig. 7(a). The resonant capacitor, \( C_r \), is placed on the VM-side in order not to block dc current component in the PWM boost converter.

Various kinds of high step-up converters with a tapped inductor (TI) have been proposed for renewable energy sources (34)-(35). Similar to conventional boost converters, a...
3. Operation Analysis

3.1 Operation Mode

The operation analysis is performed with the premise that a forward voltage drop of $D_f$ is zero, and the capacitance of $C_f$ is large enough so that its voltage $V_{CF}$ is constant. All circuit elements are assumed ideal unless otherwise noted. Deadtime periods are neglected. The transformer winding connected to the resonant capacitor $C_f$ is defined as the primary side, and the opposite side is defined as the secondary side.

Characteristic mismatch conditions vary depending on seasons, time, locations, etc. This section takes a representative case that PV3 only is partially shaded. Key operation waveforms and current flow directions are shown in Figs. 8 and 9, respectively.

Mode 1 ($0 < t \leq T_1$) [Fig. 9(a)]: $Q_L$ and $Q_H$ are turned on and off, respectively. The voltage of $L$, $v_L$, is $V_{panel} - V_{CF}$. The current of $L$, $i_L$, linearly increases, and $C_f$ is charged. The input voltage of the LLC-VM or $v_{QL}$ is zero. $L_{kg}$ and $C_f$ resonate, and the current of $L_{kg}$, $i_{L_{kg}}$, sinusoidally changes. Meanwhile, the current of $L_{mg}$, $i_{L_{mg}}$, linearly decreases. The difference between $i_{L_{kg}}$ and $i_{L_{mg}}$ is transferred to the secondary side in the form of $i_{VM}$ and flows through the coupling capacitor $C_3$ and the low-side diode $D_5$. The operation moves to the next mode when $i_{L_{kg}}$ becomes equal to $i_{L_{mg}}$.

Mode 2 ($T_1 < t \leq T_2$) [Fig. 9(b)]: $i_{L_{kg}}$ and $i_{L_{mg}}$ are equal and linearly decrease, while no current flows on the secondary side. In other words, the VM is inactive in this mode. The operation of the PWM boost converter in this mode is identical to that in Mode 1.

Mode 3 ($T_2 < t \leq T_3$) [Fig. 9(c)]: $Q_L$ and $Q_H$ are turned off and on, respectively, and the diode $D_f$ conducts. $v_L$ becomes $V_{panel} - V_{out}$, and its voltage polarity is reversed. $i_L$ starts linearly decreasing, and $C_f$ is discharged. It is noteworthy that $i_L$ as well as $i_C$ flow toward the load through $D_f$. $C_f$ is sandwiched by two voltage sources of $V_{in}$ and $V_{out}$, and therefore, its current response $i_{CF}$ in this mode is characterized by a time constant $\tau = C_f/R$ (where $R$ is the total resistance of the current flow path of $i_{CF}$).

$v_{QL}$ equals to $V_{panel}$, and $L_{kg}$ and $C_f$ resonate again. $i_{VM}$ on the secondary side flows through $C_3$ and the high-side diode $D_6$. The operation moves to the next mode when $i_{L_{kg}}$ and $i_{L_{mg}}$ become the same value.

Mode 4 ($T_3 < t \leq T_s$) [Fig. 9(d)]: Both $i_{L_{kg}}$ and $i_{L_{mg}}$ linearly increase at the same rate, while no current flows on the secondary side, similarly to Mode 2. The operation of the boost converter in this mode is identical to that in Mode 3. Turning on and off $Q_L$ and $Q_H$, respectively, brings the operation back to Mode 1.

In summary, $D_5$ and $D_6$, which are connected in parallel with the shaded substring PV3, conduct, whereas other diodes...
3.2 Voltage Conversion Ratio of PWM Boost Converter

The theoretical boost conversion ratio can be derived from the volt-sec balance on \( L \). Under steady-state conditions, \( V_{CF} \) is determined by the following equation from the current paths in Modes 3 and 4 [see Figs. 9(c) and (d)]:

\[
V_{CF} = V_{out} - V_{panel} \tag{1}
\]

From (1), \( v_L \) is expressed as

\[
v_L = \begin{cases} 
V_{panel} - V_{CF} = 2V_{panel} - V_{out} & \text{(Mode 1, 2)} \\
-V_{CF} = V_{panel} - V_{out} & \text{(Mode 3, 4)} 
\end{cases} \tag{2}
\]

This equation implies that \( L \) can be miniaturized as \( v_L \) is lower than that in a traditional boost converter, thanks to \( V_{CF} \). The volt-sec balance on \( L \) yields the theoretical voltage boost conversion ratio, as

\[
\frac{V_{out}}{V_{panel}} = 1 + d \cdot \tag{3}
\]

where \( d \) is the duty cycle of \( Q_L \). This equation suggests the theoretical conversion ratio is between 1.0 and 2.0, which is narrower than that of traditional boost converters.

The voltage conversion ratio can be extended by the integrated converter with the TI [see Fig. 7(b)], as

\[
\frac{V_{out}}{V_{panel}} = 1 + d(1 + N_{TI}) \cdot \tag{4}
\]

where \( N_{TI} \) is the turn ratio of the TI. This equation suggests the TI extends the boost conversion ratio.

The voltage conversion ratios of the proposed integrated converter are shown in Fig. 10. Unlike the traditional boost converter with a boost ratio of \( 1/(1 - d) \), conversion ratios of the proposed converters are proportional to \( d \). Although the proposed converter cannot cover a wide range (especially in the case without the TI), the boost conversion ratio and range can be extended by employing the TI and enhancing the value of \( N_{TI} \).

3.3 Miniaturization of Inductor in Boost Converter

A charged or discharged energy \( E_{CD} \) of an inductor in a single switching cycle is generalized in the form of

\[
E_{CD} = \int_{0}^{dT_s} |v_L i_L| \, dt = \int_{0}^{T_s} |v_L i_L| \, dt \tag{5}
\]

where \( v_L \) and \( i_L \) are the voltage and current of an inductor. \( E_{CD} \) reflects a ripple current of the inductor. Therefore, energy stored in the inductor \( E_L \) can be expressed using a ripple current factor \( \alpha \) (typically around 30%), as

\[
E_L = \frac{E_{CD}}{\alpha} \tag{6}
\]

The normalized inductor energy, \( S \), can be defined as

\[
S = \frac{E_L}{E_{out}} = \frac{E_{CD}}{\alpha V_{out} I_{out} T_s} \tag{7}
\]

where \( E_{out} \) is the energy transferred to a load in a single switching cycle. In general, a volume of inductors is dependent on its stored energy, and hence, inductor volumes of different topologies can be quantitatively compared based on calculated values of \( S \).

Under steady-state conditions, an average current of \( C_f \) must be zero, and therefore, an average inductor current \( I_L \) equals to the output current \( I_{out} \):

\[
I_L = I_{out} \cdot \tag{8}
\]

It is noteworthy that \( I_L \) of the proposed converter is lower than that in a traditional boost converter \( I_L = I_{out}/(1-d) \). This is because a fraction of the output current or output power is processed by \( C_f \), as can be seen in Figs. 9(c) and (d)—\( I_{CF} \) flows toward the load.

Substitution of (2) and (8) into (5) and (7) yields

\[
S = \left( \frac{d}{\alpha(1 + d)} \right) \tag{9}
\]

Substituting (3) into (9) yields \( S \) as a function of \( V_{out}/V_{panel} \). The calculated \( S \) of the proposed integrated converter is compared with that of a traditional boost converter, as shown in Fig. 11. \( \alpha \) is 0.3 (i.e., 30%). Although the voltage conversion ratio of the proposed converter is theoretically limited between 1.0 and 2.0, the proposed converter achieves lower \( S \), suggesting the reduced inductor volume in the proposed integrated converter. The inductor miniaturization is attributed to that \( C_f \) reduces \( v_L \) and \( I_L \) compared with those of traditional boost converters [see (2) and (8), respectively].
3.4 LLC Voltage Multiplier

The LLC-VM is powered by the panel and produces ac current that is transferred to the secondary side of the transformer. $d$ of the boost converter varies depending on conditions, but the characteristics of the DPP converter should desirably be unaffected by duty cycle variations.

As can be seen in Figs. 8 and 9, $i_{VM}$ and $i_D$ flow only in Modes 1 and 3 whose lengths are equal to half the resonant period. In Modes 2 and 4, on the other hand, the secondary side of the LLC-VM is essentially inactive [see Figs. 9(b) and (d)], and lengths of Modes 2 and 4 vary with duty cycle variations. In other words, duty cycle variations can be buffered in Modes 2 and 4.

In order for Modes 2 and 4 to exist, half the resonant period must be shorter than both $dT_s$ and $(1-d)T_s$, where $T_s$ is the switching period. Therefore, the switching frequency $f_s$ should satisfy the following equations

$$
f_s \leq \begin{cases} 
2d_{fr} = \frac{d}{\pi \sqrt{L_{kg}C_r}} \\
(1-d)f_{fr} = \frac{1-d}{\pi \sqrt{L_{kg}C_r}}
\end{cases} \quad (10)
$$

where $f_{fr}$ is the resonant frequency. This equation suggests that $f_s$ should be determined with taking into account a variation range of $d$ at a given $f_{fr}$.

The VM on the transformer secondary side is driven by ac voltage and current generated by the LLC resonant inverter. A square wave voltage $v_s$ with a peak-to-peak value of $V_{panel}/N$ is generated across the transformer secondary winding. Since coupling capacitors, $C_1$–$C_3$, are connected to the secondary winding, ac current components only pass through $C_1$–$C_3$ and flow toward substrings. A dc voltage equal to the peak-to-peak value of $v_s$ (i.e., $V_{panel}/N$) is outputted to each substring because each capacitor and diode pair (e.g., $C_1$, $D_1$, and $D_2$) comprises a voltage doubler (30).

Focusing only on the ac components yields the equivalent circuit, as shown in Fig. 12, in which all substrings, as well as diode rectifiers containing coupling capacitors, are separated and grounded. Since all substrings, as well as rectifiers, are connected in parallel, currents from the LLC-VM automatically and preferentially flow toward shaded substrings having the lowest voltage in the panel. This automatic current distribution equalizes the electrical characteristics of all substrings (30).

4. Experimental Results

4.1 Prototype

A 150-W prototype of the proposed integrated converter that can supply 28 W for each shaded substring was built for four substrings connected in series, as shown in Fig. 13. Component values are listed in Table 1. For the integrated converter to operate with a duty cycle variation range of 0.2–0.8 at $f_s = 100$ kHz, $f_{fr}$ was designed to be 273 kHz, according to (10).

4.2 Measured Waveforms and Power Conversion Efficiency

The experimental setup shown in Fig. 14 was employed to measure operation waveforms, power conversion efficiencies, and output characteristics of the LLC-VM of the prototype. An external power supply was used as the input voltage $V_{in}$. A variable resistor $R_v$ was connected to the output of the VM to emulate the current flow paths under the partial shading condition where PV 1 is shaded. The current of $R_v$, $I_{Rv}$, corresponds to the current supplied to the shaded substring in practical use. Power conversion efficiencies and $V_{out}$ were measured with varying $I_{Rv}$ while the load power $P_{out} (= V_{out}I_{out})$ was fixed to be 0, 70, or 140 W at $V_{in} = 32$ V and $d = 0.5$.

Measured waveforms at $I_{Rv} = 1.0$ A and $P_{out} = 70$ W are shown in Fig. 15. The good agreement with the theoretical ones shown in Fig. 8 demonstrated the proper operation of the prototype.
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The measured power conversion efficiencies and $V_{R}$, as a function of $I_{R}$, were independent on $P_{out}$ (see the bottom panel in Fig. 16), suggesting that the output characteristics of the LLC-VM were independent on the operation of the boost converter.

The measured efficiencies increased with $P_{out}$, as shown in the top panel in Fig. 16. The case of $P_{out} = 0$ W was equivalent to the operation of the LLC-VM alone and the output power of the boost converter was zero. Therefore, the power conversion efficiency of the LLC-VM alone was lower than 75%. Meanwhile, the case of $I_{R} = 0$ A corresponded to the operation of the boost converter alone, and therefore, the power conversion efficiency of the boost converter was approximately 96% at $P_{out} = 70$ W or 140 W. These results suggested that the boost converter was more efficient than the LLC-VM. The most dominant loss factor in the LLC-VM was the diode conduction loss as its output voltage of $V_{R}$ was relatively low (lower than 8 V). Another major loss factor was the copper loss of the transformer because of the relatively large RMS value of $i_{VM}$, which flows only in Modes 1 and 3 (see Fig. 8).

4.3 Laboratory Testing Emulating Partial Shading Condition

An experimental equalization test using solar array simulators (E4361A, Keysight Technologies) in our laboratory was performed emulating the partial shading condition where PV1 was shaded. Individual substring characteristics used for the laboratory experiment are shown in Fig. 17(a)—the characteristics were swept using an electronic load and measured by a data logger (NR-500, KEYENCE), and MPPs are highlighted with markers. If all substrings were to be able to operate at each MPP in this condition, the maximum power of the panel as a whole would have been 55.5 W. $V_{out}$ was fixed to be 48 V by an electronic load operating in a constant voltage mode, while $d$ was manually varied in the range of 0.3–0.7 to sweep panel characteristics. A bypass diode was connected in parallel with each substring to compare panel characteristics with/without the integrated converter.

The measured $P$–$V$ characteristics of the panel with/without the integrated converter are shown in Fig. 17(b). Without the integrated converter, two MPPs (a global and local MPPs) on the $P$–$V$ characteristics were observed, and the maximum power was 45.7 W. With the integrated converter, conversely, the local MPP disappeared, and the maximum power increased to as high as 54.0 W, corresponding to 18.2% improvement in power yield. The overall efficiency was approximately 97.3% (54.0/55.5 W). The results demonstrated that the proposed converter could eliminate partial shading issues.

4.4 Field Testing Emulating Characteristic Mismatch Condition

The field testing was performed using two flexible panels (CVFM-0580T1-WH, Clean Venture 21), each consisting of two substrings, in Hitachi, Japan, at 15:00
The maximum power without the integrated converter was merely 30.4 W that was equivalent to the overall efficiency of approximately 96% (≈ 43.0/44.6 W). With the integrated converter, the local MPP successfully disappeared, and the maximum power reached as high as 43.0 W. This result corresponds to 41.4% improvement in power yield and the overall efficiency of 68.2% (≈ 30.4/44.6 W). Thus, the proposed integrated converter reduced the negative influences of the characteristic mismatch due to uneven irradiance on PV substrings in the field testing.

5. Conclusions

The PWM boost converter incorporating an LLC-VM-based DPP converter has been proposed in this paper. This converter can not only boost the panel voltage by PWM control but also automatically supply power to the shaded substrings without feedback control. The proposed converter not only simplifies the system by integrating two converters into a single unit but also achieves the reduced switch count as well as inductor miniaturization in comparison with a conventional system using a boost converter and DPP converter separately.

Laboratory and field testing using the prototype was performed emulating partial shading and irradiance mismatch conditions. The proposed integrated converter eliminated local MPPs and enhanced the maximum power by 18.2% and 41.4% in the laboratory and field tests, respectively, demonstrating its efficacy.

Fig. 18. Experimental setup of field testing using flexible PV panels

Fig. 19. Experimental results of field testing. (a) Individual substring characteristics. (b) Panel characteristics with/without integrated converter

on April 2019, as shown in Fig. 18. The flexible panels were intentionally bent in order to roughly emulate a mismatched irradiance condition on PV panels mounted on a PHEV’s roof. The solar irradiance was measured using a pyranometer (ES-602, EKO), and the average solar irradiance in the field testing was 495 W/m². Similar to the laboratory testing, V_out was fixed to be 48 V, and d was varied in the range of 0.3–0.7.

Measured individual substring characteristics in the field testing are shown in Fig. 19(a). The mismatched irradiance on the flexible panels resulted in severe characteristic mismatch, and the ideal maximum power was 44.6 W in this condition.

The measured P–V characteristics of the panel with/without the integrated converter are compared in Fig. 19(b). The maximum power without the integrated converter was merely 30.4 W that was equivalent to the overall efficiency of

References


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