Combined Pulse-Width Modulation of Dual Active Bridge DC–DC Converter to Increase the Efficiency of Bidirectional Power Transfer

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A dual active bridge (DAB) dc–dc converter is suitable for bidirectional high-power transfer. However, it has a limited soft-switching range and a high peak current during low power transfer with traditional phase-shift modulation (PSM) when there is a variation in the input/output voltage. To overcome solve this difficulty, this paper proposes a new modulation strategy, combined pulse-width modulation (CPWM) which combines single pulse-width modulation (SPWM) and dual pulse-width modulation (DPWM), with a unified fundamental phase ϕ for a wide power transfer range. Unlike previous modulation strategies, CPWM is applicable for bidirectional power transfer. In order to combine SPWM and DPWM, this study comprehensively analyzes the soft-switching range and the peak current of PSM, SPWM, and DPWM. The CPWM strategy is experimentally tested using a 1.6 kW prototype to verify the improvement in efficiency and its bidirectional power transfer capability. The experimental results show efficiencies of both positive and negative power transfers were improved by using CPWM.

Keywords: dual active bridge, bidirectional power transfer, modulation strategy, soft-switching range, pulse-width modulation

1. Introduction

Renewable energy systems, including photovoltaic systems and wind power generation systems, have been rapidly increasing in number. Since these systems cannot provide stable power, their unstable outputs should be compensated by integrating the renewable energy systems with energy storage systems (ESS) to use them in utility grids. Bidirectional dc–dc converters form the cores of the ESS. They are widely used not only for renewable energy systems but also for hybrid car systems and uninterrupted power supplies. There are two types of bidirectional dc–dc converters: isolated and nonisolated. If a large voltage gain is required, the non-isolated converters require more switches and bigger capacitors (6)–(15). The isolated dc–dc converters can easily achieve large voltage gains by adjusting the winding ratio of the transformer. The isolated dc–dc converters come in many types (15). In this study, a single-phase dual active bridge (DAB) converter is chosen because of its soft-switching capability, bidirectional buck-boost operation, and simple circuit configuration (15).

Most of the literature on DAB converters focus on the optimal modulation strategy to operate in a wide range of voltage variation and in a wide power range (15)–(23). The DAB converter modulation has three degrees of freedom (ϕ, τP, τS). The phase ϕ is the phase shift between the square voltage of the two bridges. The primary and secondary voltage widths (τP, τS) are the phase shifts between the legs within a bridge. The phase-shift modulation (PSM) is the most commonly used strategy to regulate power flow because it has only one degree of freedom (ϕ). When the input or the output has a wide voltage variation, the traditional PSM has a limited soft-switching range and a high circulation current at a low power range (15). The single pulse-width modulation (SPWM), which is also known as the trapezoidal modulation (16), has two degrees of freedom (ϕ, τP). It can reduce the peak current and slightly increase the soft-switching range at a medium power range (17)–(18). The dual phase shift (DPS) modulation also has two degrees of freedom (ϕ, τP = τS) but it is used to reduce high inrush current (19). The dual pulse-width modulation (DPWM), which is also known as the triangular modulation or the triple phase shift, has three degrees of freedom (ϕ, τP, τS) (20). It can increase the soft-switching range and reduce the high circulating current at a low power range.

These modulations can be combined to form a modulation strategy to extend the soft-switching range. Great improvement in efficiency especially at low power transfer has been reported (20)–(24). The modulation strategy proposed in (21) has analyzed modulations that can operate with the optimal efficiency, although the determination of the three degrees of freedom is unclear. The determination of the three degrees of freedom is important to ensure a smooth transition between the modulations. The modulation strategies in (20) (22) (23) have an algorithm of the three degrees of freedom to operate in the soft-switching. Especially, the modulation strategy in (23) has mathematically determined the three degrees of freedom to operate in soft-switching even when there is a wide voltage variation. However, the algorithm of these modulation strategies are only able to transfer power to one direction. The algorithm requires improvement to transfer power bidirectionally. Since the DAB transfers power bidirectionally, it
is important for the modulation strategy to transfer power to negative direction too.

This paper proposes an algorithm of a new modulation strategy, combined pulse-width modulation (CPWM) to transfer power smoothly and bidirectionally in a wide voltage variation by combining the SPWM and the DPWM with a unified fundamental phase \( \varphi \). This paper includes analyses of the PSM, the SPWM, and the DPWM including power transfer, soft-switching range, and peak current to operate with a low peak current even if there is a wide voltage variation in the input or the output voltage. This paper also discussed on how the DPWM can increase the soft-switching operation.

This paper is organized as follows: Section 2 introduces the DAB dc–dc converter. Section 3 analyses the power transfer, peak current, and soft-switching range of the three types of modulations. The CPWM is presented in Section 4. The experimental results are presented in Section 5 to demonstrate the advantages of the CPWM.

2. DAB DC–DC Converter

Figure 1(a) displays a DAB dc–dc converter circuit. It consists of primary and secondary full bridges (FB1 and FB2), and a transformer constructed with a relatively high leakage inductance \( L_1 \). Each power switch (S1–S8) has a parasitic capacitor and an antiparallel diode. In the primary bridge, S1 and S2 are in leg 1, and S3 and S4 are in leg 2. In the secondary bridge, S5 and S6 are in leg 3, and S7 and S8 are in leg 4. FB1 converts the primary dc voltage supply \( V_{dc1} \) to a 2 or 3 level square ac voltage (primary square voltage \( v_p \)) by switching four power switches (S1–S4). FB2 converts the secondary dc voltage supply \( V_{dc2} \) to a 2 or 3 level square ac voltage (secondary square voltage \( v_s \)) by switching the other four switches (S5–S8). The primary and secondary windings of the transformer (\( N_p \) and \( N_s \)) determine the primary and secondary square voltages, and the potential difference results in the leakage inductance voltage \( v_{Li} \). Shifting the fundamental phase \( \varphi \) between the middle of primary and secondary square voltages regulates the power flow\(^{20}\). The details are discussed in section III. The current \( i_{L1} \) flowing through the transformer depends on the values of the leakage inductance \( L_1 \) and the switching frequency \( f_s \). The equivalent circuit for the ideal converter is given in Fig. 1(b).

3. Modulation Analysis

A modulation consists of the phase \( \varphi \), the primary square voltage \( v_p \), and the secondary square voltage \( v_s \). Figure 2 shows that the modulation strategy determines the switching waveform for each power devices S1–S8. Although the power transfer depends on the value of the fundamental phase \( \varphi \), the power analysis is done based on \( \varphi \) because it simplifies the subsequent explanation. Figures 1(c) and 1(d) demonstrate the difference between the phase \( \varphi \) and the fundamental phase \( \varphi_1 \). The relation between \( \varphi_1 \) and \( \varphi \) can be expressed by

\[
\varphi_1 = \varphi + \frac{\tau_S - \tau_P}{2}, \quad \frac{\tau_P}{2} \leq \tau_S \leq \frac{\tau_P}{2} \tag{1}
\]

In this section, the PSM, the SPWM, and the DPWM are compared by analyzing their power transfers, peak currents, and ZVS ranges. During the analyses, the primary voltage \( V_{dc1} \) is considered to be higher than the secondary voltage \( nV_{dc2} \). The voltage ratio \( r = nV_{dc2}/V_{dc1} \) depends on the winding ratio \( n \), the primary voltage \( V_{dc1} \), and the secondary voltage \( V_{dc2} \). The reason why the primary voltage \( V_{dc1} \) is higher than the product of the secondary voltage and the winding ratio \( nV_{dc2} \) is that we designed this algorithm under the condition that the voltage ratio is less than 1 \( (r < 1) \). The voltage ratio becomes less than 1 when the primary voltage \( V_{dc1} \) is higher than the value \( nV_{dc2} \). When we use the algorithm under the condition that the value \( nV_{dc2} \) is higher than the primary voltage \( V_{dc1} \), the higher voltage side \( nV_{dc2} \) is set to be the primary voltage and the lower voltage side \( V_{dc1} \) is set to be the secondary voltage. Then, the voltage ratio can be kept less than 1. These are necessary for studying the advantages and disadvantages of each modulation.

Another important thing to know is the soft-switching conditions. Theoretically, all power devices will operate in the zero voltage switching (ZVS), if all the leakage inductance current conditions below are satisfied:

\[
i_{L1}(0) < 0 \text{ for leg 1}, \quad i_{L1}(\tau_P) > 0 \text{ for leg 2}, \quad \frac{i_{L1}(\varphi)}{i_{L1}(\tau_S)} > 0 \text{ for leg 3}, \quad \text{and} \quad i_{L1}(\varphi + \tau_S) < 0 \text{ for leg 4} \tag{2}
\]

In the ZVS operation, negative current flows through the antiparallel diode of the power device and the charges in the parallel parasitic capacitor are completely discharged. Then, the power device is turned on while the voltage across it is
3.1 Phase-Shift Modulation (PSM) This modulation is widely used due to its simplicity of controlling the magnitude and direction of power by shifting the phase, \( \varphi \). In Figs. 1(c) and 1(d), the primary and secondary voltage widths are fixed at \( \pi \). Since the primary and secondary voltage widths are equal, the fundamental phase is the same as the phase, namely \( \varphi = \varphi_i \). The power transfer is given by

\[
P = \frac{rV_{dc1}^2}{2\pi f_s L_1} \varphi (1 - |\varphi|/\pi) \tag{3}
\]

where \( r = nV_{dc2}/V_{dc1} \) is the voltage ratio, \( n = N_p/N_s \) is the winding ratio, and \( f_s \) is the switching frequency. When \( \varphi > 0 \), power is transferred from \( V_{dc1} \) to \( V_{dc2} \), and when \( \varphi < 0 \), power is transferred from \( V_{dc2} \) to \( V_{dc1} \). At \( \varphi = \pm \pi/2 \), the converter reaches the maximum power transfer

\[
P_{\text{max}} = \frac{nV_{dc1}V_{dc2}}{8f_s L_1} \tag{4}
\]

This implies that the value leakage inductance \( L_1 \) and the switching frequency \( f_s \) affect the power transfer across the transformer. The peak current for the PSM is

\[
i_{\text{peak}} = \frac{nV_{dc2}(2\pi - \varphi) + V_{dc1}\pi}{4\pi f_s L_1} \tag{5}
\]

This confirms that the peak current is not zero even if the phase \( \varphi \) is zero when \( r \neq 1 \). There is a high peak current in the converter even if there is no power transfer between the primary and secondary sides.

Theoretically, the ZVS is satisfied if the minimum value of the phase is

\[
\varphi_{\text{min}} > \frac{\pi}{2}(1 - r) \tag{6}
\]

The ZVS capability reduces if the voltage ratio is not equal to 1 (\( r \neq 1 \)).

3.2 Single Pulse-Width Modulation (SPWM) This modulation has two degrees of freedom, which are the phase \( \varphi \) and the primary voltage width \( \tau_p \). Figures 3(a), 3(b), and 3(c) implies that the secondary voltage width is fixed at \( \pi \). The modulation is not widely used alone because the direction of the power transfer does not correspond to the value of \( \varphi \). The value of the phase \( \varphi \) is different from the value of the fundamental phase \( \varphi_f \) in the SPWM. The power transfer is given by

\[
P = \frac{rV_{dc1}^2}{4\pi^2 f_s^2 L_1} (2\varphi \tau_p - 2|\varphi|\varphi - \tau_p^2 + \tau_p \pi) \tag{7}
\]

When \( \tau_p \) is equal to \( \pi \), (7) is identical to (3). This means that the power transfer of the SPWM depends on the phase and the primary voltage width.

The peak current for the SPWM is

\[
i_{\text{peak}} = \frac{nV_{dc2}(\pi - 2\tau_p) + V_{dc1}\tau_p}{4\pi f_s L_1} \tag{8}
\]

The peak current is not zero even if \( \varphi = \tau_p = 0 \) when the voltage ratio \( r \neq 1 \). In fact, the circulation current is generated due to the presence of the secondary voltage width \( \tau_s \). The advantage of this modulation is that it has a lower peak current for medium to high power transfer than the PSM.

Reference (17) shows that only leg 3 and leg 4 are operating in hard-switching at low power. Since the leakage current of the secondary voltage at \( i_{\text{leg}}(\varphi) \) is the same as that at \( -i_{\text{leg}}(\varphi + \pi) \), one leakage current condition \( i_{\text{leg}}(\varphi) > 0 \) is enough to achieve the ZVS operation for leg 3 and leg 4. The ZVS is satisfied if the minimum value of the phase is

\[
\varphi_{\text{min}} > \frac{\pi}{2}(\frac{\tau_p}{\pi} - r) \tag{9}
\]

The converter operates in the ZVS starting from \( \varphi > 0 \) if the primary voltage width is \( \tau_p = \pi r \). In order to match \( \varphi \) of the SPWM with that of the PSM, the phase is expressed using the fundamental phase by using (1). From (1) and (9), the minimum fundamental phase for the SPWM is given by

\[
\varphi_{\text{fmin}} > \frac{\pi}{2}(\frac{1 - r}{\pi}) \tag{10}
\]

The soft-switching range of the SPWM is equal to that of the PSM based on the fundamental value. Since reducing the primary voltage width to \( \tau_p = \pi r \) reduces the power transfer, the ZVS range capability increases. For example, when the voltage ratio \( r = 0.5 \), the ZVS range of the SPWM increases up to 33% compared to the ZVS range of the PSM. Figure 3(b) displays an example of the modulation that does not satisfy the conditions (9) and (10).

3.3 Dual Pulse-Width Modulation (DPWM) This modulation has three controllable parameters, which are the phase \( \varphi \), the primary voltage width \( \tau_p \), and the secondary voltage width \( \tau_s \). The modulation is the most complex because there are many combinations of the same value of the fundamental phase \( \varphi_f \). Here, the analyzed modulation is only
in the condition that is below (10) including the waveforms in Figs. 4(a), 4(b), and 4(c). Although the modulation in Fig. 4(d) operates fully in soft-switching, the peak current is higher than the other modes of the DPWM \cite{22}. Only the modulation in legs 1, 3, and 4 are analyzed because the equations of three degrees of freedom \cite{25} are applicable to the modulations in Figs. 4(a) and 4(c) (positive and negative power transfers). The current analysis for the power transfer is given in Table 1. The power transfer is given by

\[ P = \frac{r}{4\pi f L_1} \left( 2\varphi_{\text{tr}} - \tau_p^2 + \tau_p \tau_S \right) \] \hspace{3cm} (11)

where the phase is less than zero, namely \( \varphi < 0 \). This indicates that the power transfer depends on the three parameters. The peak current for the DPWM is

\[ i_{\text{peak}} = \frac{n V_{\text{dc2}} (1 + r)(2 \varphi - 2 \tau_p)}{4 \pi f S L_1} + V_{\text{dc1}} \tau_p \] \hspace{3cm} (12)

The peak current can be reduced to zero if \( \varphi = \tau_p = \tau_S = 0 \) regardless the present voltage ratio. A lower current flowing through the transformer at low power transfer reduces the total losses in the power devices.

Reference \cite{25} demonstrates that legs 1, 3, and 4 are operating in hard-switching at low power. From Fig. 4(b), the leakage current conditions \( i_{L1}(\varphi) < 0 \), \( i_{L1}(0) > 0 \), and \( i_{L1}(\tau_S) < 0 \) are needed to maintain the ZVS operation for all the power devices.

The ZVS condition for leg 1 is satisfied if the minimum value of the phase is

\[ \varphi_{\text{min}} < \frac{1}{2} \left( \frac{\tau_p}{r} - \tau_S \right) \] \hspace{3cm} (13)

The converter operates in the ZVS from \( \varphi_{\text{min}} < 0 \) if the primary voltage width satisfies \( \tau_p = \tau_S r \). The phase shift must be negative to achieve the ZVS during positive and negative power transfers. In order to match \( \varphi \) of the DPWM with that of the PSM and the SPWM, the phase is expressed using the fundamental value by using (1) and \( \tau_p = \tau_S r \). The minimum fundamental phase for the DPWM is

\[ \varphi_{\text{Lmin}} < \frac{\tau_p}{2 r} \left( 1 - r \right) = \frac{\tau_S}{2} \left( 1 - r \right) \] \hspace{3cm} (14)

In the SPWM analysis, the primary voltage width does not affect the ZVS range. This implies that only the DPWM can increase the ZVS range in terms of the fundamental phase shift by reducing the value of secondary voltage width.

The soft-switching conditions for legs 3 and 4 are satisfied if the following condition is satisfied:

\[ \tau_S > \tau_p \] \hspace{3cm} (15)

This shows that the primary voltage width must be smaller than the secondary voltage width to achieve the ZVS operation in legs 3 and 4. If the secondary voltage width is reduced, a lower power can be transferred in the ZVS.

4. Proposed Modulation Strategy

In this section, the SPWM and the DPWM are combined into one modulation strategy to operate in the ZVS and to reduce the circulating current. The modulation strategy is named the combined pulse-width modulation (CPWM). The waveform transition of the CPWM is shown in Fig. 5. The modulations are classified according to two fundamental phase intervals for positive and negative power transfers. The equations of three degrees of freedom (\( \varphi, \tau_p, \tau_S \)) are calculated with the fundamental phase \( \varphi \) and the voltage ratio \( r \) for each fundamental phase interval. Distinct from other modulation strategies, the bidirectional power flow is achieved by considering the equation for the phase \( \varphi \).
4.1 Fundamental Phase Interval Classification  The analyses in Section 3 explain that the DAB dc–dc converter operates in the ZVS in a wide power range with the SPWM or the DPWM when the fundamental phase reaches the conditions in (10) or (14), respectively. This indicates that the modulations have their advantages and disadvantages in a particular fundamental phase range. In order for the CPWM to operate in the ZVS in a wide power range, the fundamental phase is divided into two intervals.

The first interval is for low to mid power transfer and the second interval is for mid to high power transfer. Each interval will consider the positive and negative power transfers. The boundary between the first interval and the second interval depends on the voltage ratio \( r \). This equation (10) implies that the boundary changes according to the voltage ratio \( r \). The smaller the voltage ratio, the longer the first interval of CPWM.

The first fundamental phase interval for positive and negative low power transfer is

\[
\text{Interval 1: } -\frac{\pi}{2}(1 - r) < \varphi_f < \frac{\pi}{2}(1 - r). \tag{16}
\]

This interval is suitable for the DPWM because (14) proves that the soft-switching can be extended. In addition, it has the lowest current and power rating.

The second fundamental phase interval for positive and negative low power transfer is

\[
\text{Interval 2: } -\frac{\pi}{2} \leq \varphi_f \leq -\frac{\pi}{2}(1 - r). \tag{17}
\]

The second interval is suitable for the SPWM and the PSM because (6) and (10) depict that both modulations can operate in the ZVS. The SPWM is used for the mid power transfer because it can transfer lower power and current with the same fundamental phase. The PSM is used for the full power transfer because it can transfer the highest power. Then the three degrees of freedom are calculated based on this fundamental phase interval classification.

4.2 CPWM Equation  In this section, the three degrees of freedom, such as \( \varphi, \tau_p, \) and \( \tau_s \), are calculated based on the fundamental phase \( \varphi_f \) and the voltage ratio \( r \) for both intervals. The equations of the CPWM are summarized in Table 2. Figure 6 demonstrates the three parameters for \( r = 0.5 \).

In the interval 1, the parameters for the DPWM are calculated. When \( \varphi_{\text{min}} < 0 \) and (13) are considered, the relation between the primary and secondary voltage widths is

\[
\tau_p = rt_S, \tag{18}
\]

Although (13) stated that the phase must be negative, the phase is set to zero to avoid a discontinuous modulation strategy\(^*\). Therefore, the hard-switching will still occur in the interval 1.

From (1), (18), and \( \varphi_{\text{min}} = 0 \), the primary and secondary voltage widths can be calculated for positive and negative power transfers. The primary and secondary widths are,

\[
\tau_S = \frac{2}{1 - r} |\varphi_f| \quad \text{and} \quad \tau_p = \frac{2r}{1 - r} |\varphi_f|. \tag{19}
\]

The primary and secondary voltage widths consider the absolute fundamental phase because the primary and secondary voltage width are always positive regardless the sign of the fundamental phase. The consideration of the absolute fundamental made the calculation for the phase in the interval 1 to be possible. The phase is calculated by using (1) and (19) to be

\[
\varphi = \varphi_f - |\varphi_f|. \tag{20}
\]

When the fundamental phase and the secondary voltage width reach \( \pm \pi(1 - r)/2 \) and \( \pi \), respectively, the CPWM enters the second interval.

In the interval 2, the SPWM is combined with the PSM. In summary, the fundamental phase starts from \( \pi(1 - r)/2 \) to \( \pi/2 \), while the primary voltage starts from \( \pi r \) to \( \pi \). The linear variation and gradient comparison of the primary voltage width to the fundamental phase yields

\[
\tau_p = \frac{1 - r}{r}(2|\varphi_f| - \pi) + \pi. \tag{21}
\]

The primary voltage width in the second interval also considers the absolute fundamental phase because the primary voltage width is always positive regardless the sign of the

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Table 2. Combined pulse-width modulation equation

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<td>( \tau_s )</td>
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fundamental phase. The phase is calculated by using (12), (21), and \(r_S = \pi\) to be

\[
\varphi = \varphi_1 + \frac{(1 - r)}{2r} (2|\varphi_1| - \pi).
\]

(22)

The phase changes from 0 to 90 when the fundamental phase is 45 to 90, respectively while the phase maintains at 90 when the fundamental phase is \(-45\) to \(-90\), respectively when the voltage ratio is \(r = 0.5\).

The CPWM combines the three modulation strategies based on the soft-switching boundary and reduces the peak current for a wide power range. Figure 7 are the peak current varied with power transfer of the CPWM and that of the PSM. The dead time interval is fixed at 0.24 \(\mu\)s. All the parameters of the simulation are summarized in Table 3. The core of the transformer is a toroidal core (FINEMET, Hitachi Metals). The leakage inductance is 70 \(\mu\)H. The primary to secondary winding ratio is 11:22. The primary and secondary voltages are fixed at 380 V. This experimental configuration enables the bidirectional power transfer more than 1 kW with a single regenerative power supply. In this case, the voltage ratio \(r\) becomes 0.5. This condition is chosen to operate the converter in an unfavorable voltage variation scenario while the convenient condition is \(r = 1\). In addition, when the voltage ratio is 0.5, the boundary becomes \(\pi/4\) from (6) which is the half of the maximum power phase \(\pi/2\). It is easier to observe the difference between the DPWM and the SPWM when interval 1 (from 0 to \(\pi/4\)) and interval 2 (from \(\pi/4\) to \(\pi/2\)) have an equal range. The dead time interval is fixed at 0.24 \(\mu\)s. All the power devices (\(S_1\) to \(S_6\)) in FB1 and FB2 are SiC MOSFETs (SCH2080KE, ROHM).

An experiment is conducted to validate the bidirectional power transfer capability of CPWM. The efficiency and power transfer of the prototype are measured with a precision power analyzer (WT1800, YOKOGAWA). The entire control is implemented with a digital processor (TMS320F28335, TI).

6. Experiments

6.1 Prototype and Controller

In this research, a prototype is designed for a bidirectional power transfer of 1.6 kW. The main circuit is shown in Fig. 8. The parameters are summarized in Table 3. The core of the transformer is a toroidal core (FINEMET, Hitachi Metals). The leakage inductance is 70 \(\mu\)H. The primary to secondary winding ratio is 11:22. The primary and secondary voltages are fixed at 380 V. This experimental configuration enables the bidirectional power transfer more than 1 kW with a single regenerative power supply. In this case, the voltage ratio \(r\) becomes 0.5. This condition is chosen to operate the converter in an unfavorable voltage variation scenario while the convenient condition is \(r = 1\). In addition, when the voltage ratio is 0.5, the boundary becomes \(\pi/4\) from (6) which is the half of the maximum power phase \(\pi/2\). It is easier to observe the difference between the DPWM and the SPWM when interval 1 (from 0 to \(\pi/4\)) and interval 2 (from \(\pi/4\) to \(\pi/2\)) have an equal range. The dead time interval is fixed at 0.24 \(\mu\)s. All the power devices (\(S_1\) to \(S_6\)) in FB1 and FB2 are SiC MOSFETs (SCH2080KE, ROHM).

An experiment is conducted to validate the bidirectional power transfer capability of CPWM. The efficiency and power transfer of the prototype are measured with a precision power analyzer (WT1800, YOKOGAWA). The entire control is implemented with a digital processor (TMS320F28335, TI).

6.2 Experimental Results

In this experiment, the
power is bidirectionally transferred by shifting the fundamental phase from $-90^\circ$ to $90^\circ$ with the CPWM. Figure 11 compares the experimental data are nearly equal. This justifies the correctness of (7) and (11). The hard-switching operation is highlighted with light and dark gray shaded areas. The dark gray shaded area between $0^\circ$ to $45^\circ$ represents that the power devices in leg 1 are in the hard-switching. The light gray shaded area between $-45^\circ$ to $0^\circ$ illustrates that the power devices in leg 3 and leg 4 are in the hard-switching.

Figures 12 and 13 compare the experimentally measured efficiencies under the CPWM for a bidirectional power flow from $-1600$ W to $1600$ W. The efficiency during positive and negative power transfers improves by about 10% with the CPWM. The total power loss of the CPWM is about 50 W lesser than that in the PSM. This result confirms that the CPWM increases the overall efficiency even when there is voltage variation in the system.

Figures 14, and 15 show the experimental waveforms of the PSM and CPWM. The hard-switching points are marked with red circles. Figures 14 and 15 confirm that the peak current of the CPWM is lower than that of the PSM for medium and high power transfer ($600$ W and $1500$ W, respectively). The hard-switching operation is observed during the experiment. Figure 14(a) confirms that the leakage inductance current at $i_{L1}(\phi)$ and $i_{L1}(\phi + \pi)$ are not satisfying the ZVS conditions ($i_{L1}(\phi) > 0$ and $i_{L1}(\phi + \pi) < 0$, respectively). The leg 3 and leg 4 of the converter are operating in the hard-switching at $600$ W. Figure 15(a) confirms that the leakage inductance current at $i_{L1}(0)$ and $i_{L1}(\pi)$ are not satisfying the ZVS condition ($i_{L1}(0) < 0$ and $i_{L1}(\pi) > 0$). The leg 1 of the converter is operating in the hard-switching at $600$ W.
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\[ i_{L1}(\tau_P + \pi) > 0, \text{ and } i_{L1}(\tau_P) < 0, \text{ respectively.} \]  
The leg 3 and leg 4 of the converter are operating in the hard-switching at \(-600 \text{ W.}\)  
The surge is also not present in the positive and negative power transfer with CPWM and PSM during the hard-switching operation.

7. Conclusion

This paper has proposed a new modulation strategy, the CPWM by combining the SPWM and the DPWM to transfer power bidirectionally with low peak current even if there is a wide voltage variation. For this purpose, the SPWM and the DPWM are comprehensively analyzed. The advantages of the CPWM are verified by the simulation and the experiment with a 1.6 kW prototype. The bidirectional power transfer is successfully done with the CPWM. The experimental results confirm that the efficiency of positive and negative power transfer improves by about 10% with the CPWM. Our future research is to improve the CPWM to accomplish the ZVS operation in the interval 1.

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References


Modulation Strategy of Dual Active Bridge DC–DC Converter

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