Material removal mechanism in rotary in-feed grinding 
- Modeling and analysis -

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Abstract
The rotary in-feed grinding is the most promising process to ensure the controllability of wafer geometry including the thickness and flatness. This makes it be widely applied into manufacturing of mono-crystal wafers, such as silicon, silicon carbide, sapphire, lithium tantalate, and etc. Unlike the conventional surface grinding, however, the rotary in-feed grinding is a complex material removal operation in which the extent of interaction between the abrasives and wafer keeps varying during the wheel/wafer contact zone. Such characteristic property significantly influences the chip formation, the homogeneity of surface topography, the variations in grinding forces and the power consumption in the radial direction of the wafer. In this paper, the grinding mechanisms including the geometry, statics and surface topography were theoretically analyzed on a proposed grinding model and organized as follows. Described in Section 2 is the geometric analysis which mathematically expresses the wafer profile and a variety of information regarding to the dimensions of chip formed. The results suggest that the chip cross section increases proportionally as increasing in the wafer radial distance. Based on the results of chip formation, the grinding forces respectively exerted on an individual abrasive, a single wheel segment and whole wafer are calculated in the Section 3. Also, the necessary grinding power and generated grinding heat are estimated by statics analysis. In the Section 4, the surface roughness and its variation in a radial direction of the wafer are then derived by taking into consideration of the distribution of abrasive protrusion in height-wise.

Keywords: Rotary in-feed grinding, Cutting path, Wafer profile, Chip formation, Grinding force, Topography, Surface roughness

1. Introduction
Mono-crystal materials, such as sapphire, silicon carbide and lithium tantalate are of great interest in the fields of microelectronics, optics and aerospace, due to their extraordinary functionality. Most of electronic and semiconductor devices in our daily lives are built on the substrates or wafers of those functional materials (Pishchik et al., 2009; Gerhardt et al., 2011; Strukov et al., 1998). Mono-crystal ingots normally undergo several manufacturing processes including slicing, beveling, grinding, lapsing, polishing and etching, to be shaped into wafers (Tönshoff et al., 1990). The rotary in-feed grinding is the most promising process to ensure the controllability of wafer geometry including the thickness and flatness (King and Hahn., 1986). Wafer profile, surface roughness and subsurface damage are three major indicators to evaluate the performance of rotary in-feed grinding. Over the past years, numerous efforts have been invested to achieve better wafer profile (ex. less total thickness variation), better topography (ex. less surface roughness) and less subsurface damage.

In rotary in-feed grinding, both wafer and underneath chuck are ground into a conic shape by slightly tilting the wheel rotational axis against the wafer rotational axis, to ensure the contact length between the grinding wheel and the
wafer to be constant. Sun et al. proposed the machine configurations for the spindle axis adjustments (Sun et al., 2005), in which an important criterion is given to tilt the wheel rotation axis relative to the wafer rotation axis. Via the kinematic analysis and experiments, Tso et al. also stated that the inclination of grinding wheel axis must be set appropriately to improve the total thickness variation (TTV) of the ground substrate (Tso and Teng, 2001). In the report of (Zhang et al., 2006), Zhang et al. reviewed the hypothesis for the generation mechanisms of central dimples. Zhou et al. kinematically analyzed the cutting path of individual abrasive across the wafer surface to get the wafer profiles for different combination of tilt angles of the wheel axis against the wafer axis (Zhou et al., 2003).

Regrading to the surface roughness, Tomita and Eda developed a fixed abrasive grinding wheel for producing an ultra-fine surface on a magnetic disk substrate (Tomita et al., 1996). Liu et al. investigated the effects of different abrasive shapes and dressing to predict surface roughness in grinding using kinematic simulations (Liu et al., 2013). Our previous study (Zhou et al., 2017) has theoretically analyzed the effects of grain size variation on the wafer surface topography. The study found that the actual surface roughness could become several folds larger than the theoretical prediction.

To reduce the subsurface damage (or SSD) induced in grinding, many approaches have been made from the viewpoints of grinding wheel specifications, grinding conditions and wafer properties. Lundt et al. stated that grinding of silicon wafer would cause unavoidable SSD (Lundt et al., 1994). Pei et al. found that the depth of subsurface crack on ground silicon wafers is approximately equal to half of the diamond abrasive size used in the grinding wheel (Pei et al., 1999). Zhang et al. analytically predicted the depth of SSD in silicon wafer induced in the rotary in-feed grinding process (Zhang et al., 2019). This research stated that less SSD can be achieved with lower feed rate, lower wafer rotation speed and higher grinding wheel speed.

In order to associate the grinding conditions with the grinding results mentioned above, study on the material removal mechanism is very essential. In grinding mono-crystal materials, material removal in either ductile or brittle mode is one of the most interested issues (Doi et al., 2014). In order to realize ductile mode grinding, the depth of cut of each abrasive must be controlled to be less than the critical depth of cut (or DOC) of ground wafers. Young et al. derived the critical DOC for silicon wafers by the experimental investigation (Young et al., 2006, 2007). Our previous study showed that a better surface roughness can be achieved if the abrasive cutting depth is less than the critical DOC (Zhou et al., 2012). The study also indicated that the transition between the ductile and brittle modes can be controlled by the grinding conditions such as wheel rotational speed, wafer rotational speed and in-feed rate. Moreover, Lin et al. proposed a model to predict abrasive depth of cut in rotary in-feed grinding (Lin et al., 2018). It is claimed that the elastic-plastic behaviors in both wheel and wafer should be taken into consideration for accurately predicting the grain depth-of-cut in ultra-fine grinding. Therefore, the chip formation is a key for understanding the mono-crystal material removal mechanism. Unlike the conventional surface grinding, however, the rotary in-feed grinding is a complex material removal operation in which the extent of interaction between the abrasives and wafer keeps varying during the wheel/wafer contact zone. A further study is required to look insight into the variation in chip formation along the wafer radial direction.

The grinding force is another important issue to understand the interaction between the abrasives and the materials to be removed, and the induced subsurface damage. Couey et al. have developed a device for in-process force monitoring in precision grinding of silicon wafers (Couey et al., 2005). The research also indicated that the grinding force was influenced by the in-feed rate. Sun et al. provided a predictive model of grinding force in silicon wafer grinding, which correlated the grinding conditions to the grinding force and was validated by experimental results (Sun et al., 2016). A wireless dynamometer has been developed for rotary in-feed grinding in our previous study (Ishibashi et al., 2019). It has found that the grinding force gradually grew up when the wheel segment started to engage from the wafer center, and rapidly dropped to zero when the wheel segment exits from the wafer fringe. Therefore, a thorough analysis on the relationships between the chip formation and grinding statics becomes essential.

Based on the rotary in-feed grinding dynamics, in this paper, we mathematically analyze the cutting path of abrasive in 3D manner to obtain the wafer profile and chip formation along the cutting path. The grinding force exerted on an individual abrasive is correlated to the chip cross section, which is then extended to a single wheel segment and whole wafer. Also, the consumed grinding power and generated grinding heat are estimated for such grinding dynamics. In the rest sections of this paper, the number of effective cutting edge is estimated by taking into consideration of the distribution of abrasive protrusion in height-wise, to derive the resultant roughness on the wafer surface and its variation in a radial direction of the wafer.
2. Geometric analysis
2.1 Rotary in-feed grinding model

The rotary in-feed grinding model is illustrated in Fig.1. A wafer is mounted via a porous vacuum chuck and a cup-type grinding wheel is placed opposite with a tilt angle $\alpha$ and an offset equivalent to the wheel radius $R_1$. During grinding, both grinding wheel and wafer rotate around their own axes simultaneously while the wheel is fed into the wafer with a given in-feed rate $f$. Such in-feed grinding dynamics can keep the contact area unchanged and thereby to deliver a stable grinding performance throughout the grinding process. The symbols used in model description are listed in Table 1, where the common parameters used in wheel and wafer specifications and grinding conditions are also specified for the subsequent calculation. Listed in Table 2 are symbols used in model analysis and evaluation.

In a Cartesian coordinate system with the origin fixed at the wafer center, our previous study (Zhou et al., 2003) has found out the mathematical description of the cutting path for an abrasive to travel across the wafer surface in 2D space. Taking the tilt angles into the consideration, the cutting path description is then updated to 3D space and given in a matrix form as Eq. (1).

\[
\begin{bmatrix}
    x(\tau) \\
    y(\tau) \\
    z(\tau) \\
    1
\end{bmatrix} = A \cdot B \cdot C \cdot D \cdot E \cdot F
\begin{bmatrix}
    x_g \\
    y_g \\
    z_g \\
    1
\end{bmatrix}
\]  

(1)

where $(x_g, y_g, z_g)$ is the initial position of corresponding abrasive and $\tau$ is the grinding time. The matrices $A$ and $E$ represent the rotations of wafer and wheel respectively, and are specified as;

![Fig.1 Rotary infeed grinding.](image)

<table>
<thead>
<tr>
<th>Table 1 The symbols used in model description and parameters for specifications and conditions.</th>
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<tbody>
<tr>
<td><strong>Wheel specifications</strong></td>
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while the matrices $B$ and $C$ respectively express the tilt angles of wafer around X-axis and Y-axis, are given as;

$$B = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & \cos \alpha & -\sin \alpha & 0 \\ 0 & \sin \alpha & \cos \alpha & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$

$$C = \begin{bmatrix} \cos \beta & 0 & \sin \beta & 0 \\ 0 & 1 & 0 & 0 \\ -\sin \beta & 0 & \cos \beta & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$

and, the matrices $D$ and $F$  are the translations of the offset $R$. 

Table 2  The symbols used in model evaluation.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Description</th>
<th>Unit</th>
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<tbody>
<tr>
<td>$r$</td>
<td>Wafer radial distance</td>
<td>[mm]</td>
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<tr>
<td>$\Delta$</td>
<td>Cutting depth</td>
<td>[mm]</td>
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<tr>
<td>$S$</td>
<td>Side area of ground wafer</td>
<td>[mm$^2$]</td>
</tr>
<tr>
<td>$N_e$</td>
<td>Density of effective cutting edge</td>
<td>[particle/mm$^2$]</td>
</tr>
<tr>
<td>$a_{eb}$</td>
<td>Size of chip cross section</td>
<td>[mm$^2$]</td>
</tr>
<tr>
<td>$q_{eb}$</td>
<td>Chip volume</td>
<td>[mm$^3$]</td>
</tr>
<tr>
<td>$f_t$</td>
<td>Grinding force on an individual abrasive</td>
<td>[N]</td>
</tr>
<tr>
<td>$F_t$</td>
<td>Grinding force on a wheel segment</td>
<td>[N]</td>
</tr>
<tr>
<td>$\Sigma F_t$</td>
<td>Total grinding force on the entire wafer</td>
<td>[N]</td>
</tr>
<tr>
<td>$q$</td>
<td>Power density</td>
<td>[W/mm$^2$]</td>
</tr>
<tr>
<td>$P$</td>
<td>Grinding power</td>
<td>[W]</td>
</tr>
<tr>
<td>$R_s$</td>
<td>Surface roughness</td>
<td>[$\mu$m]</td>
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Figure 2 shows a typical example of the cutting path in 3D manner, calculated at $\alpha \approx 6.5^\circ \times 10^{-4}, \beta = 0^\circ$. From the group of these cutting paths, we are able to get the cross-sectional profile of the wafer as shown in the bottom panel of Fig.2. It is also found whenever $\alpha \neq 0^\circ$ that the wafer profile falls in a convex cone shape so that the wheel constantly makes a half-contact with the wafer surface during grinding, which is favored in most wafer manufacturing.

2.2 Chip formation

Figure 3 shows the engagement of the wheel against the wafer. The shadow band illustrated in (a) represents the volume removed in one rotation of the wheel, where the depth of cut is $\Delta = f/n_2$ and the travel length is $L = 2\pi r_2 n_2 / n_1$. It is clear that the depth of cut is kept constant once the grinding conditions are given. Here, the wafer is considered as a collected concentric annulus with different radius $r_2$. One annulus positioned at $[r_2, r_2 + dr_2]$ is zoomed up in Fig. 3 (b) for the detailed study. The side area $S_2$ removed in one rotation of the wheel is expressed as Eq. (2).

$$S_2 = \Delta \cdot L = 2\pi \cdot \frac{f}{n_1} \cdot r_2 = 2\pi \cdot \frac{n_2}{n_1} \cdot \Delta \cdot r_2$$

(2)

As shown in Fig. 4, the side area $S_2$ is proportional to the wafer radial distance $r_2$, the depth of cut $\Delta$ and the rotational speed ratio $n_2/n_1$. It should be noted that the wafer rotational speed $n_2$ does not affect the size of $S_2$, but does affect its geometric shape. As illustrated in-panel in Fig. 4, increasing in $n_2$ leads to a long and thin shape of $S_2$ while keeps the area of $S_2$ unchanged.

According to the principle of continuity, $S_2$ is shared by the number of effective cutting edges $j = 2\pi R_1 w \cdot N_e$ which are evenly distributed across the wheel working surface, where $w$ is the width of wheel segment, $N_e$ is the density of effective cutting edge (Zhou et al., 2017). Therefore, the chip cross section removed by an individual abrasive cutting edge at the wafer radial distance $r_2$ is given below.

$$a_g = \frac{S_2}{j} = \frac{1}{R_1 w \cdot N_e} \cdot f \cdot r_2 = \frac{1}{R_1 w \cdot N_e} \cdot \frac{n_2}{n_1} \cdot \Delta \cdot r_2$$

(3)

Also, the chip volume $q_g$ and average chip cross section area $\bar{a}_g$ are then given below;
As shown in Eq. (3) and Fig. 3 (c), the chip cross section $a_g$ proportionally increases as the abrasive goes from the wafer center to its outer periphery. Here, the effective cutting edge density $N_e$ is an important factor to determine the chip cross section $a_g$ and for the rest analysis. Our previous paper (Zhou et al., 2017) has theoretically studied the distribution of protrusion height $t$ of abrasives on the wheel working surface and expressed as below;

$$N(t) = \begin{cases} 
\frac{3V_g}{4\pi r_0^2} \frac{1}{1+3} \left[ \frac{n_2}{n_1} \right] \left( \frac{m}{r_0} \right)^2 \left( 1 - \text{erf} \left( \frac{m}{\sqrt{2}r_0} \right) \right) & (0 \leq t < 2\kappa(r_0 - m\sigma)) \\
\frac{3V_g}{8\pi r_0^2} \frac{1}{1+3} \left[ \frac{n_2}{n_1} \right] \left( \frac{m}{r_0} \right)^2 \left( 1 - \text{erf} \left( \frac{m}{\sqrt{2}r_0} \right) \right) & (t \geq 2\kappa(r_0 - m\sigma)) 
\end{cases}$$

(6)

where, the abrasive radius $r_g$ is assumed to follow a normal distribution $(r_0, \sigma)$ and truncated by $r_0 \pm m\sigma$. The multiplier $m = 1$ is favored by most wheel manufacturers. The abrasive drops out when its protrusion height $t$ exceeds $2\kappa r_g$ from the bond base, where $\kappa$ takes $0 \leq \kappa < 1$ and referred as the protruding coefficient. It is found out that the number of abrasives $N(t)$ are constant when the protrusion height ranges $0 \leq t < 2\kappa(r_0 - m\sigma)$, then decreases as increasing in $t$ and eventually converges to zero at the outmost periphery of the wheel. Figure 5 shows the normalized variation of $N(t)$ for three kinds of wheels with different standard deviation $\sigma$ in the distribution of abrasive size.

Obviously, only those abrasives which have top protrusion possibly become effective to engage into material removal. The amount of these effective cutting edges can be calculated by integration of $N(t)$ within the range of $t_{max} - \Delta < t < t_{max}$ which is illustrated by a light shade in Fig. 5 and expressed in Eq. (7).

$$N_e(\Delta) = \int_{t_{min}}^{t_{max} - \Delta} N(t) dt$$

(7)

where $t_{max} = 2\kappa(r_0 + m\sigma)$ for a given protrusion coefficient $\kappa$ and abrasive radius distribution $m\sigma$. For most rotary in-feed grinding as shown in Fig.1, $\Delta$ ranges from submicron to several microns, depending on the in-feed rate $f$ and the wafer rotational speed $n_2$. Therefore, only a fraction of abrasive at the top protrusions are possibly engaged in material removal and become effective cutting edges. For such a wheel ($R_1 = 150$ mm, $w = 3$ mm, $V_g = 25$ vol%, $r_0 = 6$ $\mu$m, $\sigma = 1.5$ $\mu$m, $\kappa = 0.3$), the cutting edge exposed on the working surface is about 1000 p/mm². The
effective cutting edge density $N_e$ is calculated and shown in Fig. 6. Depending on the depth of cut $\Delta (= f/n_2)$ and standard deviation $\sigma$ in the distribution of abrasive size, $N_e$ ranges $20\sim200$ p/mm², or about $2\%\sim20\%$ of the total.

Using the above $N_e$, the chip cross section area $a_g$ is calculated as a function of $(r_2, n_2/n_1)$. The results for $\Delta = 1\mu$m is shown in Fig. 7. The chip cross section by an individual abrasive cutting edge increases linearly with an increasing in both rotational speed ratio $n_2/n_1$ and wafer radial distance $r_2$. Choosing a high wheel speed effectively constrains the chip cross section.

3. Statics analysis
3.1 Grinding force

When an abrasive is engaged in material removal, as illustrated in Fig. 8, its cutting force in tangential direction (along cutting path) is the product of the specific removal energy $C_p$ and the chip cross section $a_g$. Therefore, based on the Eq. (3), the tangential force on an individual abrasive is expressed as Eq. (8).

$$f_t = C_p \cdot a_g = \frac{C_p}{R_1 w N_e} \cdot \frac{n_2 \Delta}{n_1} \cdot r_2$$

(8)

Here, Eq. (8) addresses that the tangential force $f_t$ not only is effected by the grinding conditions $n_2/n_1, \Delta$, but also by the wheel specifications $R_1, w, N_e$, and the cutting position $r_2$ as well. Figure 9 plots two sets of tangential force $f_t$ appearing at different wheel rotational speed $n_1$ as a function of the grinding time $\tau$. It is found that $f_t$ is halved when the wheel speed $n_1$ is doubled. The iteration of $f_t$ is also doubled in frequency. It should be point out that the specific removal energy $C_p$ depends on not only the property of wafer material but also the size of $a_g$. A smaller $a_g$ normally results in relatively larger $C_p$ because of the “size effect”

Within the annulus positioned at $[r_2, r_2 + dr_2]$ mentioned above, the sum of number of abrasives totally engaged is
given as \( N_e \cdot w \cdot dr_2 \). Thus, the tangential grinding force on a wheel segment is calculated as Eq. (9).

\[
F_t = \int_{r_2-l/2}^{r_2+l/2} f_t \cdot N_e w dr_2 = CP \frac{l}{R_1} \cdot \frac{n_2}{n_1} \Delta \cdot r_2
\]  

(9)

where, \( l \) is the length of a wheel segment. \( F_t \) is found to be proportional to the segment length \( l \), speed ratio \( n_2/n_1 \) and the depth of cut \( \Delta \). By taking the effect at the wafer center and fringe, where the wheel segment is not fully contacted with the wafer, into consideration, variation of the tangential grinding force on a wheel segment \( F_t \) is shown in Fig. 10. \( F_t \) gradually grows along the wafer radial distance \( r_2 \), and rapidly drops to zero when the wheel segment exits from the wafer fringe. This finding matches well with our previous experiments on the silicon wafer grinding (Ishibashi et al., 2019). This fact also suggests that the wheel rotational direction is important for wafer thinning process to ensure the wheel segment traveling from the wafer center toward its fringe. Otherwise, the wafer may encounter a sudden increase in grinding force and highly risk in edge cracking.

### 3.2 Grinding power and grinding heat

The total grinding force \( \Sigma F_t \), generated on the entire wafer surface is calculated by integration of \( F_t \) within the range of \([0, R_2]\) which is expressed in Eq. (10).

\[
\Sigma F_t = \int_0^{R_2} f_t \cdot N_e w dr_2 = \frac{CP}{2} \cdot \frac{R_2^3}{R_1} \cdot \frac{n_2}{n_1} \Delta
\]  

(10)

The consumed grinding power is then the product of \( \Sigma F_t \) and the wheel rotational velocity, expressed as;

\[
P = \Sigma F_t \cdot 2\pi R_1 n_1 = CP \cdot \pi R_2^2 \cdot f
\]  

(11)

Both the total grinding force \( \Sigma F_t \) (the solid line in Fig. 11) and the consumed grinding power \( P \) (the broken line in Fig. 11) are proportional to the square of the wafer size \( R_2 \). Hence, it is necessary to pay attention to the rigidity and the spindle power of the grinding machine when grinding large diameter wafers.

Back to the Fig. 3 (b), the grinding power consumed on the annulus positioned at \([r_2, r_2 + dr_2]\) is expressed as;

\[
dP = 2\pi R_1 n_1 \cdot f_t \cdot N_e w dr_2 = 2\pi Cp \cdot f \cdot r_2 dr_2
\]  

(12)

The corresponding surface area ground on the annulus is given as \( 2\pi r_2 \cdot n_2 \cdot dr_2 \). The power density \( q \) (or, the energy consumed on a specific area of the wafer per unit time) is then resulted as;

\[
q = \frac{dP}{2\pi r_2 n_2 dr_2} = Cp \cdot \frac{f}{n_2} = Cp \cdot \Delta
\]  

(13)

The power density is the source of the grinding heat and directly associated with the temperature rise in the wafer. It is found that the power density is consistent across the entire wafer, without depending on the radial distance \( r_2 \). The temperature distribution, however, may not be homogeneous due to the concentricity of round shape of the wafer. The details will be evaluated by use of FEM and reported in our subsequent study.
4. Topography analysis
Illustrated in Fig. 12 is the surface topography generated on a ground wafer surface. Assuming the vertex angle of an abrasive is \(2\gamma\), the penetration depth of the abrasive into the wafer can be easily derived once the chip cross section \(a_{g}\) is known.

\[
\frac{1}{2} R_x \cdot 2 R_z \tan \gamma = a_g
\]

(14)

Substituting Eq. (3) and (7) into the above equation, the surface roughness can be solved as;

\[
R_z = \left( \frac{1}{\tan \gamma R_1 W} \cdot \frac{n_2}{n_1} \cdot \frac{a}{N_c} \cdot R_2 \right) \cdot \frac{1}{R_2}
\]

(15)

It must be pointed out that \(R_z\) in Eq. (14) is governed by the cross section \(a_{g}\), while \(a_{g}\) in Eq. (3) is the averaged cross section at the radial distance \(r_2\) without counting the variation in the abrasive radius and protrusion height. Therefore, \(R_z\) in Eq. (15) is a sort of equalized maximum surface roughness, smaller than that defined by ISO.

For such triangularly waved topography, the mean line roughness average \(R_a\) is given as;

\[
R_a = \frac{1}{L} \left( \frac{1}{2} L \cdot \frac{R_z}{2} \right) = \frac{1}{4} R_2
\]

(16)

where, \(L\) is the evaluation length. At the wafer center where the wafer radial distance \(r_2 \rightarrow 0\), in addition, K. Ono suggested that the surface roughness should take its ultimate limit (Ono, 1962). The surface roughness \(R_z\) calculated at \(\Delta = 1\mu m\) is plotted in Fig. 13, as a function of \((r_2, n_2/n_1)\). The results suggest that the surface roughness becomes larger at the outer circumference of the wafer when \(r_2\) approaches to \(R_z\), but smaller as decreasing in the rotational speed ratio \(n_2/n_1\). The trend matches well with the experimental results on silicon wafers in our previous report (Ebina et al., 2018). Figure 14 shows the surface roughness \(R_z\) calculated at \(r_2 = 50\) mm while \(\Delta\) and \(n_2/n_1\) varies.
results tell that inadequate depth of cut $\Delta$ may not improve the surface roughness due to the insufficient effective cutting edge (Fig. 6). Especially when $\Delta$ is extremely small ($\Delta < 0.2 \mu m$ for the current condition), as illustrated in Fig.12 (b), the wafer surface may not be completely removed in one rotation of the grinding wheel. The subsequent grinding still makes the surface roughness remain relatively high. Finally, it should also be noted that the Eq. (15) does not include the elastic recovery of the wafer after grinding, which could counter up to 40% ~ 60% of the depth of cut according to (Huang et al., 2020).

5. Conclusions

In this paper, the grinding mechanisms including the geometry, statics and surface topography were theoretically analyzed for the rotary in-feed grinding dynamics. The obtained results can be summarized as below:

1) The cutting path was mathematically solved in 3D manner to incorporate the tilt angles of wheel rotational axis against the wafer rotational axis, which is able to provide not only the trajectory of each abrasive but also the profile of ground wafers.

2) The chip cross section and chip volume were given along the cutting path to understand the chip formation associated with grinding conditions and wheel specifications.

3) The effective cutting edge density was estimated by taking into consideration of the distribution of abrasive protrusion in height-wise, which helps to precisely predict the chip cross section, grinding forces and surface roughness.

4) The grinding force on a wheel segment was proportional to the segment length, gradually grew along the wafer radial distance, and rapidly dropped to zero when the wheel segment exited from the wafer fringe.

5) Both total grinding force and consumed grinding power were found to be proportional to the square of the wafer size. This result suggested the rigidity and the spindle power of the grinding machine were important when grinding large diameter wafers.

6) The power density which was directly associated with the temperature rise during grinding was consistent across the entire wafer, without depending on the radial distance.

7) The surface roughness and its variation in a radial direction of the wafer were derived and discussed on varying grinding conditions. The depth of cut should be adequate to ensure sufficient cutting edge being effective.

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