Submicron Bonding Technology with Passive Alignment for Optical Modules

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Abstract

In the field of optical module assemblies, there is a demand for the passive alignment method, which is considered to contribute both to the cost reduction and the throughput enhancement. Although the optical modules require submicron level bonding accuracy, the conventional bonder can offer no better than ±5 µm level accuracy. In this paper, upgrading technologies for passive alignment bonder are reported, i.e. enhanced recognition accuracy of fiducial marks which are placed on the electrode surfaces of LD chips and silicon substrate respectively, anti-vibration structures, and anti-thermal expansion solutions. On the third issue, particularly, a new calibration method has been developed to compensate the thermal expansion that causes 1 µm positional shifting at 1°C temperature change in the atmosphere or of the machine. Verification of test results on mounting accuracy is also reported.

Key Words: Passive Alignment, Flip Chip Bonder, Heat Expansion, Calibration, Optoelectronic Device, MEMS

1. Introduction

As shown in Fig. 1, in the recent field of optical module assemblies, there is an increasing demand for replacing the conventional active alignment method with the passive alignment method, which is widely considered to contribute to the module assemblies both in cost reduction and throughput enhancement at the same time. Optical modules require submicron level flip-chip bonding of laser diode elements onto silicon substrates as the basis for fixing the optical fibers.1,2

2. Passive Alignment Method by Two-Sight Camera

In the first place, the process how the LD chips are aligned and mounted on silicon substrates using passive alignment is briefly explained. As shown in Fig. 2, the chip before bonding is held by the bonding head and the substrate is also held on the bonding stage. In order to perform alignment, the two-sight camera unit is inserted between the chip and the substrate, and it recognizes their positions, enabling any positional shift to be adjusted by the stage x, y, θ-movement. After the retraction of camera unit, the bonding head...
is lowered and then heating, pressurization, bonding (defusing) and cooling processes take place in due order.

The employment of two-sight camera has two advantages. Firstly, not only the heat tool but the heated stage of nontransparent material such as ceramics can be used to allow optimal bonding conditions for temperature profile control. Secondly, there are no such requirements as chips having well-polished surfaces for carrying out fluoroscopic recognition of alignment marks.

3. Upgrading Alignment Accuracy of Chip Bonding

Next, it will be discussed on how the range of ±5 μm accuracy for conventional flip-chip bonders has successfully been enhanced to the submicron level. The detailed explanations for this improvement are shown in sections 3.1 through 3.4. In pursuing the submicron level accuracy, it is necessary to investigate countermeasures to deal with both issues of vibration and thermal expansion. The thermal expansion issue is particularly essential. Bonding is achieved by aligning the chip and the substrate at the submicron level, but the accuracy of this bonding process cannot be ensured simply by enhancing the accuracy of the recognition process and of the alignment table mechanism. This is because heating and compression are applied from the top and bottom at a high temperature of 350°C, when eutectic bonding is performed using an Au/Sn bump material. The positional shifting caused by thermal expansion easily exceeds the required alignment accuracy.

3.1 Enhanced Recognition Accuracy

The first point is to enhance the recognition accuracy.

1) Upgraded resolution of vision system

One of the most effective measures employed in the system is “vector matching method” to improve image-processing accuracy. By achieving a greater amount of vision information correction, the vector matching method realizes ten times the accuracy of the conventional pattern matching method, and this ensures 0.05 μm accuracy of fiducial mark recognition without reducing 0.6 mm square field of view, as shown in Fig. 3.

2) Recognition tolerance for fiducial mark status

Second element is the reading tolerance affected by the distorted or chipped markings. Numerous tests have been conducted and the reading tolerance is verified to be available when marks are reduced to 75%, 60% or even 50%.

3) Correction of lens distortion

The third element considered is the correction of lens distortion. Recognition accuracy within 0.14 μm is obtained in any area of the lens by applying unique software that records and corrects lens distortion, as shown in Fig. 4.

4) One-sight alignment

Enhancement of the accuracy of the image processing and reduction of the lens distortion discussed as above also contributes to enlarging the recognition area of the camera, which enables alignment to be performed without moving the camera. With this method, there is no need to be concerned about the mechanical tolerance of the recognition camera along the X and Y axes.

3.2 Anti-Vibration Structure

There are two types of vibration: one that is caused by the alignment table operation, and the other that is caused by outside factors. For the submicron machine performance, both types of vibration may impact the operation.
1) Active anti-vibration system

"Active anti-vibration system" is installed. This system not only reduces vibration caused from outside factors but has a function to generate reverse phase vibration so as to cancel the vibration created from inside the machine. The combination of the two functions completely eliminates vibration in the X, Y and Z directions.

2) Anti-vibration measures for servomotors

Furthermore, the alignment table employing the linear servomotor tends to hunt by as much as ±1 pulse; therefore it cannot be denied that it would affect the accuracy. To solve this, alignment tables are composed of two sections: the rough and the final alignment tables. After ceasing servomotor operation of the rough alignment table, the final alignment table, using piezo actuator performs the alignment. In this way, the hunting effect of servomotor is eliminated.

3.3 Anti-Thermal Expansion Solution

1) Thermal expansion of heating tool

Here, the various effects of heat are discussed. The temperature of the heater during the bonding process is as high as 350°C, causing thermal expansion of the heater, the chip, the substrate, and the heated stage. The thermal expansion changes into the pressure on the solder bumps and causes chip misalignment. The heat generated by the heater is accumulated and conducted to the bonding head and the stage, resulting in the warp of the equipment itself. Thermal expansion of the heater affects not only in the height (Z direction) but also in the X and Y directions, so even if high-accuracy alignment is achieved, shifting may occur during bonding process. As shown in Fig. 5, when heated up to 350°C, the conventional heater causes distortional shifting of 25 μm in the Z direction and 5 μm in the X and Y directions.

The high response ceramic heater is specially developed for this particular bonding equipment. The structure of this ceramic heater is shown in Fig. 6. By employing a special laminated multi-layer structure for this ceramic heater, thermal expansion and distortion are successfully minimized, while maintaining high response. Thanks to the heat insulation structure, it is as low as 33°C at the point 25 mm below the mounting surface (heater tool tip shown at the top side of the figure) where the temperature is at 218°C. Adjusting thermal expansion coefficient to be the same among the laminated multi-layers, distortionless structure of the heater is obtained in addition to the high response and thermal insulation structure.

This structure also reduces heater elongation from 25 μm in the Z direction due to thermal expansion down to 5 μm. As less distortion occurs in bending direction, shifting in the X and Y directions (at the center of tool-tip where the chip is suction-held) is also reduced from 5 μm to 0.4 μm, as shown in Fig. 6. The influence of misalignment in the X and Y directions resulting from tool warping is of particular importance for high positional accuracy. Regardless of the degree of the machining accuracy, the tool becomes elongated and distorted –like melted candy– during heating, thus causing misalignment of the chip. However, as the thermal expansion coefficient for each layer among multi-layer structure is identical, warping does not occur, and misalignment in the X and Y directions is minimized.
2) Cancellation mechanism of thermal expansion

An air floating bonding head like the one shown in Fig. 7 was developed to absorb total elongation of heat tool, heat stage, chip and substrate. This head is equipped with an air slide in its sliding section, holding the head in a perfectly floating state by providing counter-directional compressed air so that the weight of the head and tool is counterbalanced. Heating is done in such a floating state that the total thermal expansion of the chip, the substrate, the heat tool and the heated stage is cancelled. The pressurization level can be controlled within 5±1gf. Fig. 7 also shows the temperature profile during bonding process and the amount of head elongation due to thermal expansion.3)

When the solder bumps are heated and begin to melt in the case of soldering process, the floating state enables the reduced and controlled pressurization even in the presence of the thermal expansion. And, the solder bump height is kept controlled by height sensor, until the cooling process and solidification starts.

3) Thermal expansion solution of camera unit

The effects of heat are not limited to the vicinity of the heater, the chip and the substrate. The two-sight camera unit, which is located between the two heaters, is also severely affected by the heat. As shown in Fig. 8, the upper and lower image fields are grabbed by individual CCD cameras via respective prisms. This camera box itself is affected by heat, and as can be seen in the illustration, once the power is turned on, the temperature varies by about 5°C during a day operation. It is also obvious that the bonding error of 5 μm is magnified in proportion to the temperature variation.
This error caused by heat expansion is thought to turn into the positional recognition errors in alignment process. A calibration method has been developed that corrects the thermal drift caused by heat expansion. Next, the two-sight camera calibration method that uses reference marks will be discussed.

4) Calibration using two-sight camera unit, as shown in Fig. 9

In order to detect any discrepancy between the position where the chip is recognized and the position where the chip is actually bonded, a jig provided with a reference mark is fixed on the side of the bonding head. The reference mark is printed on a transparent glass plate so that it can be observed from above and below using two-sight camera. The upper camera first gets the image of the reference mark at the head height at which the recognition of the chip mark is done. Then, the reference mark image is taken by the lower camera at the position where the head is lowered to perform bonding. Thus, the positional error is figured out as the difference between the positions recognized by the upper and lower cameras.

By feeding back the calibration data to the next bonding, the alignment accuracy is maintained.

While the calibration method described above is still effective for maintaining bonding accuracy within a few microns, it is not applicable for submicron level accuracy. The repeatability of the conventional method, as shown in Fig. 10, is in the range of ±0.8 µm, which is insufficient for submicron level alignment accuracy.

This method has three potential issues. The first is that the upper camera must retract after reading the reference mark, and then the lower camera moves in again to take its image. In short, the issue is the repeatability of this in-and-out movement of the camera. The second issue is that the jig with the reference mark is moved in and out by a cylinder during the calibration process, and this also involves the risk of error occurrence. The third one is that the recognition image must be taken by the camera with the head in an overhanging position, which differs from the actual chip bonding.

5) Calibration using three-camera system, as shown in Fig. 11

A solution to these issues is a new calibration method that uses three cameras to determine the discrepancy between the positions where the chip and the substrate are recognized and the position where bonding actually takes place. In this method, a glass substrate with reference marks is fixed next to the heated stage. In the same way as the conventional method, a
standard glass chip with reference marks is suction-held on the head. The two-sight camera unit goes between the standard chip and standard substrate, and then reads the fiducial marks with its upper and lower cameras. The relative positional differences of the upper and lower fiducial marks are then stored in memory. Next, the camera unit is retracted, the head is lowered, and the standard chip is mounted onto the standard glass substrate.

As shown in Fig. 12, the third camera reads fiducial marks on the chip and the substrate, and then recognizes the original position (with the image passing through the glass substrate). Positional errors are obtained by comparing the original position with the relative alignment position that has been stored in the recognition unit. This is the range of shifting caused by thermal expansion. In the subsequent bonding, this shifting is corrected by changing the relevant software parameters.

The Fig. 13 explains that the repetitive accuracy of this calibration method using three cameras, comparing with the calibration based on conventional reference marks, has sufficient reliability in keeping the tolerance of \( \pm 0.1 \mu m \) for submicron level bonding.

6) Automatic calibration against camera temperature variation

The fluctuations in the temperature of the camera during repetitive bonding are shown in Fig. 14. Because the camera is frequently being positioned close to the heaters, there is considerable temperature fluctuation even within a single bonding operation. The temperature of the camera rises as it approaches the heaters and falls as it retracts. The camera is subject to gradual temperature changes in the repetitive alignment operations, and this suggests the necessity of calibration for every alignment cycle.

In order to make the camera system less susceptible to the thermal influence, air is constantly supplied of which temperature is controlled within the range of 0.08°C. This air conditioning system prolonged the required interval of calibration to approximately one hour.

Fig. 15 shows the optical axes shifting converted into the bonding position during operation. As can be seen, a temperature variation of 0.5°C at the 70th bonding triggered a calibration, and shifting of 0.5 \( \mu m \) in the bonding position was canceled to zero. As shown in Fig. 16, the system is set to automatically perform this
calibration each time temperature variation of 0.5°C is detected by a thermometer installed on the camera system. Thus, it is possible to maintain high alignment accuracy during operation. Fig. 17 shows an actual example of a day operation. By performing calibration automatically for any 0.2°C variation in temperature, which is called self-correction function, alignment accuracy within ±0.2 µm is certified.

3.4 Solution of Positional Shifting during Bonding

1) Horizontal repeatability of bonding head

In a “stepper” application for example, improvement of the optical system and the positioning table as described above would be sufficient. In the bonder application, however, it is not the case. No matter how accurate the initial alignment is, there remains the issue of positional shifting during actual chip bonding, where the chip is lowered into place and pressurized.

Now, the repetitive accuracy of bonding head descending shall be discussed. This is because of the inferior repeatability of the Z-axis (vertical) guide for the head. In the bonder application, the rigidity of the guide unit has been increased, and the repeatability within ±0.025 µm is obtained.

2) Co-planarity of bonding head against substrate

Secondly, the co-planarity of bonding head will be discussed, since this factor has impact on the parallel accuracy between the chip and the substrate. The chip and the substrate should be exactly in parallel when bonded, otherwise the chip may slip out of place.

Therefore, this system has been designed so that bonding surface orientation of the head can be controlled by 2-axis NC with the resolution of 0.2 µm. This function is also effective when exchanging bonding tools according to the chip size, enabling to correct subtle deviation from co-planarity in some applications such as multi-chip and multi-position bonding.

4. Verification of Mounting Accuracy

Next, in order to verify bonding accuracy, alignment and bonding test is executed using the standard glass chip and the standard glass substrate. The bonding misalignment is measured with the camera positioned below the substrate. The test result is shown in Fig. 18, where mounting accuracy in 125 times of bonding operations is measured with bonding pressurization of 0.98 N (100 gf) and without heating. High accuracy at submicron level is maintained: 0.12 µm in the X-direction and 0.09 µm in the Y-direction, 3 sigma (σ).

Furthermore, bonding accuracy is verified with heating at 350°C in the same way. The result is shown in Fig. 19, where deviations of values in both the X and Y directions are within ±0.2 µm. In the Y-direction, however, the actual bonding position is shifted from the desired bonding position, the cause of which is thought to be the thermal expansion.

Lastly, the result of soldering interconnection of silicon chip with silicon substrate is discussed. An infrared camera is used for post-bonding inspection, which enables the direct observation of the bonded...
chip and substrate. As shown in Fig. 20, the bonding accuracy at the temperature of 280°C, lower than the melting point of the solder, is within the range of ±0.3 μm. On the other hand, the bonding accuracy at 300°C, higher than the melting point, is within the range of ±0.5 μm as shown in Fig. 21. The difference of ±0.2 μm between these results is thought to be caused by the melted solder that gave a pulling-effect in hardening process.

5. Conclusion

In order to achieve submicron level bonding for optical modules using Au/Sn technique, improvement in the accuracy of fiducial mark recognition and implemented countermeasures against vibration and thermal expansion are effective and essential. Moreover, an automatic calibration function which maintains submicron level bonding even in an environment where some temperature variations are present. Bonding accuracy within the tolerance of 0.1 μm has been achieved at room temperature. And, for soldering operations using TEG chips, bonding accuracy within the tolerance from ±0.3 μm to ±0.5 μm has been achieved. It is proven from the experimental results, however, that heating and soldering still affect bonding accuracy. Making the best use of the calibration method as presented in this paper, a day operation can be maintained with automatic calibration against such inevitable temperature variation and secure bonding accuracy of submicron level without fail.

The next step in the development of these technologies is to go beyond optical module bonding and to apply these high-accuracy bonding technologies as presented here, for other fields, such as for MEMS and 3D semiconductor packages.

References


Fig. 19 Mounting accuracy test with heating at 350°C

Fig. 20 Bonding accuracy at 280°C

Fig. 21 Bonding accuracy at 300°C