Effect of temperature on preventing electromigration damage based on increasing threshold current density in a thin metal passivated line

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1. Introduction

The reliability of electronic devices as they become smaller is a big concern. As the cross-sectional area of a thin film line gets smaller and smaller, it causes an increase in the current density and electromigration (EM) damage in the metal lines. EM is the phenomenon of atomic transport in a metal line when the line is stressed with high current density, which causes EM damage called voids and hillocks. Voids are created by the depletion of atoms and lead to disconnections of lines. Hillocks are created by the accumulation of atoms and cause short circuits. Passivation covering the metal lines has been used as one of the ways to prevent EM damage because it constrains the formation of hillocks and voids at the same time owing to the conservation of mass. The passivation characteristics such as hardness affect the ability to inhibit EM damage. EM damage is significantly affected by temperature, and to reveal the relation of the passivation hardness with temperature contributes to improving EM reliability. In addition, other factors are thought to affect the temperature dependence of EM damage. In the present paper, we investigate the temperature dependence of EM damage measured by means of threshold length product and clarify several factors affecting the temperature dependence of EM damage. A tetraethyl orthosilicate layer is used as passivation on a conventional Al line-type structure sample. Through the experiments, we demonstrate that EM damage can be prevented much more by decreasing temperature and the increase of the passivation hardness with decreasing temperature is assumed to have an influence on the ability of passivation to prevent EM damage. We also discuss the effective bulk modulus and the electrical resistivity of Al as the factors affecting the temperature dependence of EM damage.

Key words: Electromigration, Passivation, Hardness, Threshold current density, Temperature
covered with passivation, the relation between \( j_{th} \cdot l \) and temperature was presented, which indicated that \( j_{th} \cdot l \) was lower and there was more EM damage at higher temperature (Blech, 1976 and Atakov, et al., 1994). Many factors are thought to affect the temperature dependence of \( j_{th} \cdot l \).

In the present paper, we discuss the temperature dependence of \( j_{th} \cdot l \) more deeply to improve EM reliability. Since the passivation hardness is predicted to relate to the temperature dependence of \( j_{th} \cdot l \), we investigate the temperature dependence of \( j_{th} \cdot l \) and the passivation hardness through two types of experiment: the EM test and the passivation hardness test. We also discuss the effective bulk modulus \( \kappa \) and the electrical resistivity \( \rho \) of Al as the factors affecting the temperature dependence of \( j_{th} \cdot l \). The conventional Al line-type structure sample and a tetraethyl orthosilicate (TEOS) passivation layer are used in this work.

### 2. Theoretical background

The atomic flux induced by EM is generated in the direction of electron flow because of high current stressing. Passivation inhibits the formation of hillocks and voids at the same time owing to the conservation of mass. In a passivated line, the gradient of the hydrostatic stress builds up because the atomic density decreases at the cathode side and increases at the anode side owing to atomic diffusion. This gradient generates backflow, which is the atomic flux in the opposite direction of EM (Blech, 1976). EM damage is prevented because the backflow weakens the atomic flux induced by electron flow. The atomic flux in a passivated line is expressed as follows (Huntington and Grone, 1961, Weyer, 1973, Lloyd, et al., 1982, Korhonen, et al., 1993 and Sasagawa, et al., 2002a):

\[
|J| = \frac{N D_0}{k T} \exp \left( - \frac{Q}{k T} \right) \left[ Z^* e \rho \right] |j| - \frac{\kappa \Omega \partial N}{N_0 \partial l}, \tag{1}
\]

where \( N \) is the atomic density, \( D_0 \) is the diffusion constant, \( k \) is the Boltzmann constant, \( T \) is the absolute temperature, \( Q \) is the activation energy, \( \Omega \) is the atomic volume, \( N_0 \) is the atomic density under stress-free conditions, \( Z^* \) (<0 in the case of Al) is the effective valence, \( e \) is the charge of an electron, \( j \) is current density vector in the direction of \(-J\) and \( \partial N / \partial l \) is an atomic density gradient in the direction of \( J \). The term \([Z^* e \rho / |j|] \) shows the driving force resulting from current stressing, and \( \kappa \Omega \partial N / \partial l \) shows the backflow. In the case of \( J=0 \), two atomic fluxes induced by the driving force and the backflow are balanced, and no EM damages appear. The gradient \( \partial N / \partial l \) is generated after an adequate time and increases with the increase of \( |j| \). When \( N \) at the anode side reaches \( N_{max} \), which is the critical atomic density for hillock formation, and \( N \) at the cathode side is \( N_{min} \), which is the critical atomic density for void formation, EM damage is generated (Sasagawa, et al., 2002b). For this situation, \(|j| \) is called threshold current density \( j_{th} \). Therefore, at \(|j| = j_{th} \), \( \partial N / \partial l \) is given by

\[
\frac{\partial N}{\partial l} = \frac{N_{max} - N_{min}}{l}, \tag{2}
\]

and from Eqs. (1) and (2), \( j_{th} \) is given as

\[
j_{th} = \frac{\kappa \Omega}{N_0 Z^* e \rho} \frac{N_{max} - N_{min}}{l}. \tag{3}
\]

Equation (3) shows that \( j_{th} \) is inversely proportional to \( l \), so \( j_{th} \cdot l \) can be used to assess EM damage. It is predicted that \( N_{max} - N_{min} \) increases owing to the increase of the passivation hardness. Hence, \( j_{th} \cdot l \) will increase with the increase of the passivation hardness and EM resistance is expected to be improved.

### 3. Experimental procedure

In the present study, we performed two types of experiment: one for examining the effect of temperature on \( j_{th} \cdot l \) and another for examining the relationship between the passivation hardness and the substrate temperature. The EM test sample was the Al line-type structure covered with a passivation layer, as shown in Fig. 1(a). The enlarged
illustration of the sample at the cathode side is shown in Fig. 1(b). Figure 1(c) shows the cross-sectional view of the sample. A Si wafer was oxidized to form a 300 nm thick SiO$_2$ layer. TiN and Al were sequentially sputtered to form 300 nm thick (deposition for 18 min at 473 K) and 570 nm thick layers (deposition for 5 min 45 s at room temperature), respectively. Stripes 500 μm in length and 9 μm in width were formed by etching Al and TiN layers using photolithography, and a 10 μm slit was formed by etching Al. The TEOS layer, which was 780 nm thick, was deposited as passivation on the sample using chemical vapor deposition (CVD) (deposition for 6 min 40 s at 623 K). The passivation layer was etched to form a conductive pad to apply current and measure voltage.

The electron passes through the sample from the cathode to the anode during current stressing, as shown in Fig. 1(c). At the slit, electrons flow in the TiN line. Since the electrical resistivity of Al is approximately one twentieth that of TiN, electrons pass preferentially to the Al line. In the Al line, Al atoms are diffused in the direction of electron flow, and the accumulation and depletion of Al atoms occur at the anode side and at the cathode side, respectively.

The product $j_{in} \cdot l$ can be calculated through the drift velocity measurement, which has been reported by Blech (1976). The EM test was carried out at various substrate temperatures in the range of 473 to 663 K to examine the temperature dependence of $j_{in} \cdot l$. Appropriate values of current density were used to reduce the test time.

In the passivation hardness test, on the other hand, a TEOS layer was deposited as passivation on a Si wafer using CVD. The passivation hardness was measured at various values of temperature using nanoindenter (Hysitron TI 950 TriboIndenter). The indentation hardness of passivation $H$ is defined as follows (Oliver and Pharr, 1992):

$$H = \frac{P_{\text{max}}}{A},$$

where $P_{\text{max}}$ is maximum load and $A$ is projected contact area of the hardness impression. The passivation layer thickness was 1,500 nm, which was adequate for using nanoindentation (the indentation depth was less than 180 nm). In the test, a diamond tip of four-sided pyramid shape was used.

![Fig. 1 Illustrations of the EM test sample: (a) the whole image of the sample, (b) the enlargement of the sample at the cathode side and (c) the cross-sectional view of the sample.](image)

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### 4. Results and discussion

#### 4-1. The measurement of $j_{in} \cdot l$

In the EM test, voids and hillocks were formed at the cathode side and anode side of the Al line, respectively. The enlarged optical microscope image at the cathode side before the supply of current is shown in Fig. 2(a), and after the
supply of current is shown in Fig. 2(b). The drift velocity was evaluated based on the measurement of the void area. The average drift velocity \( v \) is theoretically expressed as follows (Huntington and Grone, 1961, Wever, 1973, Blech, 1976, Lloyd, et al., 1982, Korhonen, et al., 1993 and Sasagawa, et al., 2002a):

\[
v = \frac{|j|}{N} = \frac{D_0}{kT} \exp \left( - \frac{Q}{kT} |Z| \rho \Omega \frac{\partial N}{\partial l} \right).
\]

Figure 3 shows the relation of \( v \) and \(|j|\) obtained by the present EM test and the error bars indicate the standard deviation, where 4 or 5 measurements were carried out at each value of substrate temperature and current density. Since \( v \) is proportional to \(|j|\) in Eq. (5), the approximation line for \( v \) and \(|j|\) relation is assumed to be straight. As shown in Fig. 3, \( j_0 l \) was obtained by extrapolating a line to zero drift velocity. The relation of \( j_0 l \) and the substrate temperature is shown in Fig. 4. The product \( j_0 l \) increased with decreasing the substrate temperature. Therefore, the resistance to EM can be increased much more by decreasing the temperature.

![Fig. 2 The enlarged optical microscope images at the cathode side: (a) before current supply and (b) after current supply.](image)

![Fig. 3 The measured drift velocity as a function of current density.](image)
4-2. The temperature dependence of $j_n \cdot l$

For improving EM resistance, the temperature should be decreased as stated above. The clarification of the parameters affecting the temperature dependence of $j_n \cdot l$ contributes to improving EM resistance. In this work, we do not treat the change in diffusion mode with temperature based on the premise described below. Equation (5) is transformed into

$$\ln(vT) = -\frac{Q}{kT} + \ln \left[ \frac{D_0}{k} \left( |Ze| \rho |j| \frac{\kappa \Omega}{N_0} \frac{\partial N}{\partial l} \right) \right].$$  \hspace{1cm} (6)

Equation (6) can be used to obtain $Q$ from the usual Arrhenius plot. An example of the Arrhenius plot at $|j| = 0.4$ MA/cm$^2$ is shown in Fig. 5. The measured average value of $Q$ is about 0.56 eV in 473–663 K and agrees with that for grain boundary diffusion which has been reported (Blech, 1976 and Tan and Roy, 2007). The value of $Q$ invariable in the treated temperature range implies that the diffusion mode does not change.
To clarify the factors affecting the temperature dependence of $j_{th}l$, we discuss the parameters which depend on temperature in Eq. (3). Figure 6(a) shows the dependence of $H$ on the substrate temperature. An example of the relation between load and indentation displacement at 575 K is shown in Fig. 6(b). In the passivation hardness test, we carried out 4 or 5 measurements at each value of temperature. As shown in Fig. 6(a), $H$ increased with decreasing the substrate temperature. The increase of $H$ may improve the ability to inhibit the formation of hillocks with increasing $N_{\text{max}}-N_{\text{min}}$ in Eq. (3), which indicates that the increase of $H$ increases $j_{th}l$. Thus, $H$ is assumed to have an influence on preventing EM damage.

![Hardness H (GPa) vs. Substrate temperature (K)](image)

Fig. 6 The passivation hardness test using nanoindentation: (a) the dependence of the passivation hardness on the substrate temperature and (b) an example of the relation between load and indentation displacement at 575 K.

In addition, we need to consider the temperature dependence of $\kappa$ and $\rho$. It has been reported that $\kappa$ increases with decreasing temperature (Gerlich and Fisher, 1969) and $\rho$ decreases with decreasing temperature (Desai, et al., 1984). From Eq. (3), the reasons why $j_{th}l$ increased with decreasing temperature include the increase of $\kappa$ and the decrease of $\rho$ with decreasing temperature.

5. Conclusions

The temperature dependence of the threshold length product $j_{th}l$, which has been used to assess EM damage, was investigated through EM tests. In addition, the passivation hardness $H$, the effective bulk modulus $\kappa$ and the electrical resistivity $\rho$ of Al, which depend on temperature, were discussed to clarify the details of the temperature dependence of $j_{th}l$. A tetraethyl orthosilicate (TEOS) layer on a conventional Al line-type structure sample was used as passivation. The product $j_{th}l$ increased with decreasing the substrate temperature. Using nanoindentation, $H$ was found to increase with decreasing the substrate temperature. Also, the reasons why $j_{th}l$ increased with decreasing temperature include the increase of $\kappa$ and the decrease of $\rho$ with decreasing temperature. Therefore, EM damage can be prevented much more by decreasing temperature which significantly affects $H$, $\kappa$ and $\rho$.

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