A High Quantum Efficiency High Readout Speed 1024 Pixel Ultraviolet-Visible-Near Infrared Waveband Photodiode Array

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Abstract A high QE and high readout speed ultraviolet-visible-near infrared (UV-VIS-NIR) light waveband linear photodiode array (PDA) is demonstrated in this paper. For the developed 1024 pixel PDA with the pixel size of 25 ㎛H × 2500 ㎛V, seven types of high transmittance optical layers were introduced for multiply divided pixel groups to achieve high QE for receiving light waveband and multiple transfer gates were placed along the long side of PD to improve readout speed. The fabricated PDA exhibited an average QE of 70 % for 200-800 nm and 80 % for 200-320 nm wavebands, the full well capacity (FWC) of over 70 pC and the line scan period of 0.33 msec simultaneously. The condition of top surface dopant concentration of the surface p+ layer of photodiode (PD) to prevent sensitivity degradation due to deuterium lamp irradiation was also clarified.

Keywords: linear photodiode array, optical layer, UV-VIS-NIR waveband, deuterium lamp.

1. Introduction

Spectrometers are widely used in the various fields of scientific analyses, life science, environmental assessment, food inspection and so on1)~3). High sensitivity for UV-VIS-NIR light waveband is strongly required for PDA used in spectrometers to analyze various measurement samples accurately. In addition, a high readout speed performance is beneficial to improve the measurement throughput. Furthermore, high stabilities of light sensitivity and dark current of PDA toward UV-light irradiation are required for a long time use of the spectrometers without complicated maintenance. The quantum efficiency (QE) of a light detector is determined by the product of light transmittance to the photoelectric conversion part of PD and the efficiency to collect photo-generated carriers. In recent years, a Si photodiode junction technology to achieve high incident light sensitivity and high stability toward UV-light using a surface high concentration layer with steep dopant concentration profile formed on flattened Si surface has been developed, and single PD, PDA and area CMOS image sensors using the developed technology have been reported to exhibit superior characteristics4)~9). Fig.1 shows the schematic image of a spectrometry system with a diffraction grating and a PDA as spectral light detector. Spectral light is irradiated to the pre-designated positions of the PDA. For general PDAs, the dielectric film above the PD is uniformly formed across the whole light receiving surface, thus, it is difficult to optimize the transmittance for all of the receiving light waveband.

Meanwhile, image sensors with Fabry-Perot spectral filters directly formed on the top surface of monochrome CMOS image sensors have been reported to be useful to form hyperspectral line scan imagers10). The reported
sensor contains different spectral filters arranged as a staircase-like structure covering rows of pixels. It has been proposed to be applied for line scanner with scanning function. As PDAs for spectrometers, a high sensitivity is to be achieved for a wide light waveband if each PD region contains a high transmittance optical layer corresponding to the pre-designated receiving light waveband. This concept has been proposed recently11)~12).

The shape of photodiode pixel of PDA is large rectangle in order to receive spectrum light from various optical configuration of spectrometers. However, the charge readout path is generally placed on the short side of the PD. This limits the signal readout speed. In order to improve the signal readout speed of the PDA with large rectangle PDs, PDAs with multiple transfer gates along the long side of PD have been developed6).

In this work, we demonstrate a 1024 pixel PDA for UV-VIS-NIR spectroscopy with high transmittance optical layers for multiply divided pixel groups for a high QE for UV-VIS-NIR waveband and multiple transfer gates along the long side of PD for a high readout speed. In addition, the high concentration surface p+ layer with steep dopant concentration profile was introduced to improve Si incident light sensitivity and stability to UV-light irradiation. The condition of dopant concentration of the surface p+ layer to prevent sensitivity degradation due to deuterium lamp irradiation is also experimentally clarified in this paper.

2. PDA Design and Fabrication Technology

Fig.2 shows the conceptual diagram of the proposed PDA structure to achieve high QE for wide waveband. Here, pixel region is divided into several groups each contains multiple number of PDs. For each group, high transmittance optical layer is formed above PDs which is optimized toward the waveband to be received. Fig.3 shows the cross sectional viewgraph of a PD with high transmittance optical layer. For the developed PDA, pixel region was divided into seven groups having a band pass filter type optical layer each with high transmittance toward the corresponding receiving light waveband.

Fig.4 shows the circuit block diagram of the developed PDA. The pixel size is 25 µmH × 2500 µmV. Multiple numbers of transfer gates are placed along the long side of PD. (SNR) for absorption spectrometers that detect small changes of incident light levels due to absorption of target samples. For the PD, partially depleted buried pn junction was employed and the photo-generated charge is readout by charge amplifier placed outside the PDA chip. This PD structure is also beneficial to achieve a high FWC since the junction capacitance per unit area is larger in comparison to the fully depleted PD structure. For the PDA with partially depleted PD structure, the signal readout speed tends to be limited by the RC delay of PD. Thus, placing multiple numbers of transfer gates along the long side of each PD is effective to improve the signal readout speed by decreasing the RC delay6). In this work, 18 transfer gates were placed along one of the long sides of each PD.

Fig.5 shows the chip fabrication process flow and Table 1 shows the detailed structure of the seven types of optical layers. For the formation of on-chip high transmittance multiple optical layers, several design
and fabrication strategies were employed and they are summarized as below.

I. For materials of the optical layers above PDs, high integrity SiO\textsubscript{2} and small extinction coefficient Si\textsubscript{N}x were employed. These materials have long been utilized for image sensors and LSI, thus beneficial to maintain the process simplicity and long term reliability of the optical layers. The small extinction coefficient Si\textsubscript{N}x film which is transparent to UV-light waveband down to at least 200 nm was formed by plasma CVD process with optimized mixed gas flow rate and deposition pressure\textsuperscript{13-14).}

II. SiO\textsubscript{2}/Si interface formed by direct oxidation of Si was applied for all the optical layer types to maintain the high quality interface\textsuperscript{5). This is beneficial to reduce an increment of fixed charges due to UV-light exposure stress, resulting in an improvement of performance stability for a long time use of PDAs.

III. The thickness of the top surface layers of all the optical layers as well as the first and the second layers of optical layer types 4–6 were designed to be the same. This enables to apply the same films for different groups resulting in a reduction of process-induced thickness variation, leading to a reduction of transmittance variation.

IV. The designed optical layers are basically band pass filters. The transmittance at the boundary pixels of different pixel groups tend to be low. In addition, the process induced thickness variation may cause degradation of the transmittance especially at the boundary pixels. Thus, the boundaries of the different pixel groups must be carefully designed to maintain the overall high QE performance. In this work, the designed thickness differences of the optical layers; the first layers of the optical layer types 1–3 and 7 and third layers of the types 4–6, were formed by the etching process of the initially formed same films. With the in-line film thickness monitoring explained next, this design is advantageous to achieve a high tolerance toward thickness variation of the optical layers due to the film deposition process. In addition, it is expected to be effective to form multiple optical layers within the same PD around the boundaries of the pixel groups.

V. For the fabrication process, the optical layer thickness was tuned carefully using the in-line layer thickness monitoring results obtained during a sequence of the film etching process.

About the Si wafer substrate and process flow to fabricate the PDA chip, Si wafer with low concentration 20 µm-thick p-type epitaxial layer on n-type substrate was employed to improve NIR light sensitivity. The surface high concentration p+ layer with steep dopant concentration profile was formed on flattened Si surface to improve sensitivity and stability toward UV-light. The aforementioned high transmittance optical layers were formed before the metallization process. After the shield metal formation process, inter-metal dielectric film was removed on the PD regions by an etching process. In order to stop this dielectric film etching above the optical layers, an etching stopper layer was formed prior to the metallization process. Finally, the etching stopper layer was removed. The employed chip fabrication technology was based on a 0.18 µm 1P3M CMOS process with buried partially depleted PD.
3. Fabricated PDA Chip Performance

3.1 Light Sensitivity and Readout Speed

Fig. 6 shows the micrograph of the fabricated 1024 pixel PDA chip. The die size is 28.6 mm $\times$ 3.3 mm$^2$. The multiple pixel groups with different optical layers are easily recognized by its outlook. For the comparison of QE performance, a PDA with 3.8 µm-thick uniform SiO$_2$ film above all of the pixel region was also fabricated. In addition, for the comparison of signal readout speed, a PDA with single transfer gate on a short side of each PD was also fabricated with the same pixel size and process conditions.

Fig. 7 shows the photoelectric conversion characteristic of the fabricated PDA. A good linearity and a high FWC of 73 pC sufficient for absorption spectrometers were successfully obtained due to the large PD size with the buried partially PD structure. Fig. 8 (a-b) shows the measured QE as a function of wavelength of the developed PDA and the reference sample with the 3.8 µm-thick SiO$_2$ film above PD, i.e., the inter-metal dielectric of the three metal layers. For the developed PDA, the high QE performance was successfully obtained, i.e., the average and the minimum QEs (QE$_{\text{average}}$ and QE$_{\text{minimum}}$) were 70 % and 50 % for 200-800 nm waveband and 80 % and 70 % for 200-320 nm waveband, respectively. In addition, the beat of QE curve due to the optical interference appeared for the reference sample was significantly reduced in the developed PDA. It is because the optical path lengths of the optical layers formed on the developed PDA are relatively short to the wavelength of the light receiving waveband. The obtained result is to be beneficial to improve spectral sensitivity variation due to the fluctuation of optical system of spectrometers. Fig. 9 (a) shows the pixel driving pulse and (b-c) show the PD photo charge readout efficiency as a function of transfer gate on time. For this measurement, the light illumination level corresponding to the 90 % of FWC was employed. In comparison to the conventional structure with single transfer gate on the short side of PD, over 100 times faster readout speed was achieved for the developed PDA with 18 transfer gates on one of the long sides of PD. The obtained improvement of signal readout speed was well matched with the estimation using the RC delay calculation. The high FWC and high signal readout speed performances were successfully achieved by the fabricated PDA.

Fig. 6 Micrograph of the fabricated PDA chip with high transmittance optical layers for multiply divided pixel groups.

Fig. 7 Photoelectric conversion characteristic of the fabricated PDA.

Fig. 8 Measured QE as a function of wavelength of the developed PDA and a reference for (a) 200-1100 nm UV-VIS-NIR waveband and (b) 200-320 nm UV waveband.
3.2 Stability toward Deuterium Lamp Irradiation

In this subsection, the dopant concentration condition to achieve high stability toward deuterium lamp irradiation is discussed. Regarding the PD technology introduced in the developed PDA in this work that utilizes the surface high concentration layer with steep dopant concentration profile on flattened Si, the stabilities of sensitivity and dark current toward super high pressure mercury lamp were previously evaluated and studied in detail. It has been clarified that the surface dopant concentration at the top few nanometer Si surface to form a thin neutral region plays a critical role to passivate the interface states and suppress the band bending in the surface region of Si due to the electric flux of the increased fixed charges.

Deuterium lamps have different spectral distribution from super high pressure mercury lamps, and they are widely employed in various spectrometers. The degree of impact to the stability of PD’s performances may be different from those caused by the super high pressure mercury lamp. In order to clarify the dopant concentration condition to achieve a high stability of light sensitivity, p+np PDs with four p+ layer formation conditions were fabricated and evaluated. The top surface dopant concentrations of the fabricated four PDs were $1.8 \times 10^{18}$, $3.7 \times 10^{18}$, $1.1 \times 10^{19}$, $2.1 \times 10^{19}$ cm$^{-3}$, respectively.

Fig.10 shows the spectral distribution of the deuterium lamp employed for the stability evaluation. Fig.11 shows the measured changes of QE from the initial values after the deuterium lamp irradiation time of 26 h as a function of surface dopant concentration of surface p+ layer.
levels were very small. From the obtained result, the surface dopant concentration value must be higher than $10^{19}$ cm$^{-3}$ to achieve high stability to the deuterium lamp irradiation stress. For the developed PDA, the surface dopant concentration of the surface p+ layer was $1.1 \times 10^{19}$ cm$^{-3}$. Fig.12 shows the stability of QE and dark current toward the deuterium lamp irradiation. The stabilities after the irradiation time of up to 200 h were evaluated. The total amount of the light exposure after 200 h was 59, 86 and 60 J/cm$^2$ for the wavelength of 180, 204 and 240 nm, respectively. It is sufficiently long for the evaluation of performance stability for general spectrometers over 10 years. In Fig.12 (b), the obtained results under two stress light irradiation angles of 0° (perpendicular to PDA surface) and 17° (tilted toward transfer gate) are shown. Here, for the spectrometer usage, light irradiation angles on PDA are not always 0°. Since the surface high concentration p+ layer is not formed near the end of the transfer gate side, tilted irradiation angle condition is expected to be severer for the developed PDA. Fig.12 shows that the degradations of QE and dark current were very small for the all applied stress conditions due to the introduced PD junction technology. The high stability of both QE and dark current were achieved for the developed PDA.

Table 2 summarizes the developed PDA chip design specifications and measured performances. The superior performances of the developed PDA chip were successfully confirmed.

4. Conclusions

In this paper, a high QE and high readout speed 1024 pixel UV-VIS-NIR waveband PDA with high transmittance optical layers for multiply divided pixel groups and multiple transfer gates along the long side of PD was designed, fabricated and evaluated. The very high average QE of 70 % for 200-800 nm and 80 % for 200-320 nm wavebands were successfully obtained. A high FWC of over 70 pC and a high line scan period of 0.33 msec were achieved simultaneously. In addition, it was clarified that the surface dopant concentration must be higher than $10^{19}$ cm$^{-3}$ in order to achieve high stability toward deuterium lamp irradiation. The high stabilities of sensitivity and dark current of the fabricated PDA toward deuterium lamp irradiation were successfully confirmed. The developed PDA is promising for performance improvement of UV-VIS-NIR spectrometers. Furthermore, the developed optical layer formation technology is expected to be useful for developments of various types of spectral image sensors.

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