Present Status of Exposure Tool Development for Low Energy Electron-beam Proximity Projection Lithography


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A β-tool for Low Energy Electron-beam Proximity Projection Lithography (LEEPL) has been developed for proof of lithography (POL) of mass production tool, which is applied to required performance in 100-nm and 70-nm technology node. Major features of system design are an acceleration voltage of 1-5 kV, a beam current of 3-20 µA, a maximum field size of 40 mm x 40 mm (effective field size of 25 mm x 25 mm), a die-by-die alignment system and automatic loader systems for masks and wafers. The throughput is estimated over 20 wafers of 300 mm* per hour. As examples of initial evaluation results, resolution of 45-nm L/S patterns and 48-nm hole patterns in resist image were obtained. Alignment experiments are on going now, and overlay accuracy around 20-nm (3σ) over effective area of 8-in. wafer is being expected.

Keyword: LEEPL, POL, die-by-die alignment system, throughput, resolution, overlay accuracy

1. Introduction

Several next generation lithography (NGL) technologies are proposed to achieve linewidths around 70-nm and below. Though all of them have an ability to make such fine patterns just as required, they have the same issue that the lithography cost is drastically increasing because of the extreme rise of the production tool price and the running cost. LEEPL is only NGL candidate to meet the lithography requirements provided by the International Technology Roadmap for Semiconductors (ITRS), moreover to overcome this issue. This feature comes from the concepts described in section 2.

One and half year ago, proof of concept (POC) of LEEPL was successfully completed by an alpha-tool which was developed in 4 months. Now a beta-tool for 100-nm and 70-nm technology node has been developed in a year and whose evaluation has been in progress. High-resolution and alignment capability has been already obtained. Moreover, 1X stencil masks around 70-nm dimensions are achieved by utilizing mask-making technology of the proximity x-ray lithography (PXL). LEEPL has come close to practical use, so much earlier than other NGL candidates.

2. Concepts and Advantages of LEEPL

Figure 1 shows summary of concepts and advantages of LEEPL. There are three major system concepts of (a) use of a low energy e-beam, (b) use of a parallel e-beam and simple scan, (c) use of proximity projection.

Use of a low energy e-beam can drastically reduce the proximity effect caused by back-scattered electron, which is usually the biggest problem for e-beam lithography. Because the proximity effect is negligible, LEEPL has no requirement for proximity effect correction. Besides, this concept can realize low power process, which reduce the heating expansion and damage of a mask and a resist film on wafers during exposure. In addition, it is easier to make
mask patterns precisely because the mask membrane can be much thinner. Moreover, it is very easy to correct distortion of image placement on masks and wafer by changing the beam-landing angle under real-time control during exposure as described in section 4.1. These advantages promise to realize high accurate projection performance. On the other hand, the resist sensitivity can get much higher than that of any other a-beam lithography using higher acceleration voltage. So, this allows LEEPL to have high throughput capability.

Use of a parallel e-beam and simple scan can make electron optics and control system very simple. This advantage has capability to reduce the unit cost.

Finally, use of proximity projection can make space charge effect free at the path between a mask and a wafer. So, high beam current can be used for projection at this point. This advantage is another reason why LEEPL has high throughput capability.

3. System Specifications

The specifications of the β-tool, which is targeted for 100-nm and 70-nm technology generation, are shown in Table I. Major specifications of electron optics are an acceleration voltage of 1-5 kV (fixed for usual use), a beam current of 3-20 µA, a maximum field size of 40 mm x 40 mm (effective field size of 25 mm x 25 mm for the β-tool). Specifications of projection accuracy are resolution of 50-nm, overlay accuracy of 25-nm (IMI+3σ) by using a die-by-die alignment system. The throughput is estimated over 20 wafers of 300 mm² per hour. Specifications of substrates are 4-in. and 8-in. size of masks, 200 mm² or 300 mm² size of wafers, which are handled by automatic loader systems.

4. System Configurations and Design

As shown in figure 2, the β-tool consists of five major modules, which are electron optical column, alignment system with peripheral instruments, X-Y stage, automatic mask and wafer loader, and total control system.

4.1 Electron Optical Column and Control System

As shown in figure 3, the electron column consists of an e-beam gun with LaB₆ cathode, a condenser lens, two position of variable beam limiting aperture, and two sets of electrostatic-octapole deflector, which are main deflectors and sub-deflectors. The concepts of LEEPL described above can make the column very simple, just as shown in figure 4.
The e-beam source of LaB$_6$ is good enough for getting high beam current with satisfactory stability. Figure 5 shows the evaluation result of the beam current stability at 23.2 µA. The current variations of every one-hour period were less than 0.5% during 12 hours. Figure 6 shows a beam shape measured by faraday cage with pinhole mask on it. This beam shape was like columnar shape and the beam diameter was 450 µm$^2$ of FWHM.

Main deflectors are used for scanning a beam over a mask and a wafer with a stage motion by step and repeat. Sub-deflectors are used for tilting a beam as illustrated in figure 3 in order to correct distortion of pattern placement on both a mask and a wafer. Figure 7 shows an example of magnification correction in case of reduction by this way. Sub-deflectors on the β-tool have capability to control the beam-landing angle with range of ±10 mrad. Moreover, this function is very effective to correct the mechanical alignment error caused on alignment system described in section 4.2 to achieve high accurate overlay.

4.2 Alignment System

A video-based scattered-light alignment (SLA) system$^{44}$, which has been used on PXL stepper and experienced in actual device processes, is used for die-by-die alignment on the β-tool. This
alignment error less than 10-nm during full time of exposure. The residual error is corrected by sub-deflectors to tilt the beam-landing angle with a feedback loop between alignment system and deflection control system.

Major advantages of this alignment system are (a) capability to monitor mechanical alignment status directly during exposure with feedback loop to align, (b) capability to apply position sensing to various gaps between a mask and a wafer and to monitor the gap simultaneously, (c) insensitivity to multiple reflection and diffracted light between a mask and a wafer by using white light illumination. These advantages lead to high accurate alignment performance with robustness.

4.3 X-Y Stage

X-Y wafer stage for coarse motion employs non-magnetic ultrasonic linear motors, on which there is the fine stage of the alignment system described above. The whole stage system can be taken out from main chamber for maintenance because of no motor connections through the chamber wall. The X and Y tables are made of a ceramic to achieve accurate addressing of beam scan. The maximum stage velocity and the acceleration are 100 mm/s and 0.1 G. Both eight-inch and twelve-inch wafers can be loaded by changing pallets without neither any exchange of parts nor any mechanical adjustments.

4.4 Mask and Wafer Loader

A mask loader system consists of a SMIF pod opener as shown in figure 11, a pre-aligner, and a mask-handling robot in a vacuum load chamber. Four-inch and eight-inch size of masks can be loaded to the system. A SMIF pod is used as a
mask case to keep a mask particle free during handling outside of the system. In this system, slow pumping and venting functions are used for taking care of particle adhesion on a mask during loading and unloading.

The vacuum transport controller, alignment controller, and signal process controller are obeyed the main controller as shown in figure 2. The major functions of main controller are change of video signals concerned with die-by-die alignment and e-beam adjustments, vacuum control over main chamber and mask/wafer load chamber, calibration of e-beam and deflection characteristics, sub-system control including initializing, mask and wafer loading/unloading, and exposure execution. The man-machine interface is used for operations of adjustments of each sub-system, registrations of alignment models, creations and edits of recipe file of exposure, monitoring the sub-systems status, and controlling data files and parameter files.

5. Exposure Examples

5.1 Proximity Effect

The proximity effect was evaluated by using bi-layer resist process as actual device fabrication process for LEEPL. The top imaging layer was silicon-containing negative tone resist of 130-nm thickness and the bottom layer was novolac type of photo-resist of 350-nm thickness. There was not seen proximity effect as shown in figure 13. The 180-nm isolated line was fabricated with independent of patterns around it.

5.2 Resolution and line edge roughness

Figure 14 shows the resolution performance evaluated by line and space patterns in resist image. The 45-nm line and 115-nm space patterns and 70-nm line and space patterns (1:1 replication), which were almost as same size as those on a mask, were obtained. Figure 15 shows the resolution performance evaluated by hole patterns in resist image. Hole patterns of 48-nm⁴ size or below
The line and space patterns and hole patterns were fabricated by 50-nm thickness resist film of ZEP520 (Nippon Zeon Co., Ltd.). Line edge roughness (LER) was improved drastically for about a year as shown in figure 16. The resist process was quite same as that of resolution evaluation. The 3-nm of LER was obtained by the β-tool at 10 month later from initial exposure on α-tool. The others were obtained by α-tool. Improvements of both characteristic of e-beam incident angle distribution and mask pattern quality are thought as major cause for this LER improvement.

The resolution capability and LER quality were proved as satisfied for 70-nm technology node.

5.3 Overlay

Figure 17 shows example of mechanical alignment error in x-direction generated when a mask and a wafer are interlocked by alignment system and the closed-feedback loop is executed. The alignment error values were almost less than 15-nm and expected to be improved less than 10-nm by adjustments of alignment system. The residual error of this is to be corrected by sub-deflectors.

Alignment experiments are on going now, and overlay accuracy around 20-nm (3σ) over effective area of 8-in. wafer is being expected.

6. Conclusion

A β-tool for Low Energy Electron-beam Proximity Projection Lithography (LEEPL) has been developed for proof of lithography (POL) of mass production tool. Major system functions and operations were confirmed by exposure examples. As examples of initial evaluation results, resolution of 45-nm L/S patterns and 48-nm² hole patterns in resist image were obtained. Alignment experiments are on going now, and overlay accuracy around 20-nm (3σ) over effective area of 8-in. wafer is being expected. This needs further investigations.
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